

# Model-Checking Higher-Order Programs with Recursive Types

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**Abstract.** Model checking of higher-order recursion schemes (HORS, for short) has been recently studied as a new promising technique for automated verification of higher-order programs. The previous HORS model checking could however deal with only *simply-typed* programs, so that its application was limited to functional programs. To deal with a broader range of programs such as object-oriented programs and multi-threaded programs, we extend HORS model checking to check properties of programs with *recursive* types. Although the extended model checking problem is undecidable, we develop a sound model-checking algorithm that is relatively complete with respect to a recursive intersection type system and prove its correctness. Preliminary results on the implementation and applications to verification of object-oriented programs and multi-threaded programs are also reported.

## 1 Introduction

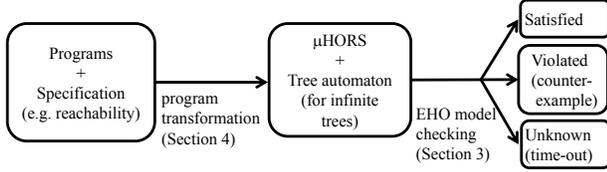
The model checking of higher-order recursion schemes (HORS for short) [33] has been recently studied as a new technique for automated verification of higher-order functional programs [22, 30, 34, 29]. HORS is essentially a simply-typed higher-order functional program with recursion for generating (possibly infinite) trees, and the goal of HORS model checking is to decide whether the tree generated by a given HORS satisfies a given property. The idea of applying the HORS model checking is to transform a given functional program  $M$  to a HORS  $\mathcal{G}$  that generates a tree describing possible outputs or event sequences of the program [22]; verification of the program is then reduced to HORS model checking, to decide whether the tree generated by  $\mathcal{G}$  represents valid outputs or event sequences. Based on this idea, various verification problems for functional programs have been reduced to it [22, 30, 34, 45]. By combining it with predicate abstraction, a software model checker for functional programs can be constructed [34, 29]. The reason why HORS model checking works well for verification of functional programs is that HORS is itself a kind of functional program, so that the control structures (higher-order functions and recursion, in particular) of functional programs can be naturally and precisely modeled.

The above approach to automated verification of functional programs, however, cannot be smoothly extended to support other important programming language features, such as objects and concurrency. Object-oriented programs

often use (mutually) recursive interfaces, which cannot be naturally modeled by HORS (which are *simply-typed* functional programs). In fact, even Featherweight Java (FJ) [15] (with only objects as primitive data) is Turing complete [41]. As for concurrency, the model checking of concurrent pushdown systems [38] is undecidable. These imply that there cannot be a sound and complete reduction from verification problems for object-oriented or recursive concurrent programs to HORS model checking. These situations are in sharp contrast to the case for functional programs, for which we have a sound and complete reduction to HORS model checking, as long as the programs use only finite base types (such as booleans, but not unbounded integers) [22].

The present paper aims to overcome the above limitations by introducing an extension of HORS model checking, where models, i.e., higher-order recursion schemes, are extended with recursive types. The extended higher-order recursion schemes, called  $\mu$ HORS, are essentially the simply-typed  $\lambda$ -calculus extended with tree constructors, (term-level) recursion, and recursive types, which is Turing complete. The model checking of  $\mu$ HORS ( $\mu$ HORS model checking for short) is undecidable, but we can develop a sound (but incomplete) model checking procedure. The procedure uses the result that HORS model checking can be reduced to a type checking problem in an intersection type system [22, 27, 46], and solves the type checking problem. Although the procedure is incomplete (as  $\mu$ HORS model checking is undecidable) and may not terminate, it is relatively complete with respect to a certain recursive intersection type system: any program that is typable in the type system is eventually proved correct. The procedure incorporates a novel reduction of the intersection type checking to SAT solving, which may be of independent interest and applicable to ordinary HORS checking.

Being armed with  $\mu$ HORS model checking, we can construct a fully automated verification tool (or so called a “software model checker”) for various programming languages, as shown in Figure 1. Given a program, we first apply a kind of program transformation to get a  $\mu$ HORS that generates a tree describing all the possible program behaviors of interest, and then use  $\mu$ HORS model checking to check that the tree describes only valid behaviors. For example, given an object-oriented program with a specification on method call sequences like “after a close method of a file object is called, no read/write methods are called”), we first construct a  $\mu$ HORS that generates a tree describing all the possible method call sequences. This is achieved by combining functional encoding of objects [6] with the reduction from verification of functional programs to HO model checking [22]. We then invoke the  $\mu$ HORS model checker to check whether the tree contains only valid method call sequences. The  $\mu$ HORS model checking can also be used to verify concurrent, higher-order programs, since we can write a scheduler in  $\mu$ HORS thanks to recursive types. The idea of applying program transformation to reduce program verification problems to HORS model checking problems is the same as our previous framework for automated verification of functional programs [22], but thanks to the expressive power of  $\mu$ HORS, we can now deal with other programming language features like objects and concurrency.



**Fig. 1.** The overall structure of our verification method

As a proof of concept, we have implemented a prototype of the  $\mu$ HORS model checker (which corresponds to the second phase of Figure 1), and a translator from Featherweight Java (FJ) programs [15] to  $\mu$ HORS (which corresponds to the first phase of Figure 1). Preliminary experiments show that we can indeed use the  $\mu$ HORS model checker to verify small but non-trivial object-oriented programs.

The rest of this paper is structured as follows. Section 2 introduces recursive intersection types and the  $\mu$ HORS model checking problem, and gives a recursive intersection type system that is sound with respect to the model checking problem. Section 3 gives an algorithm for  $\mu$ HORS model checking, and proves that it is sound and relatively complete with respect to the recursive intersection type system. Section 4 shows applications of  $\mu$ HORS model checking to verification of objects and multi-threads. Section 5 reports preliminary experiments. Section 6 discusses related work and limitations of our approach. Section 7 concludes the paper.

## 2 Preliminaries

This section introduces  $\mu$ HORS, defines model checking problems for them, and reduces it to a type-checking problem in a recursive intersection type system.

### 2.1 Recursive Intersection Types

Before introducing  $\mu$ HORS model checking, we first formalize recursive intersection types. We fix a finite set  $Q$  of base types below, and use the meta-variable  $q$  for its elements. We use the meta-variable  $\alpha$  for type variables.

**Definition 1.** A (recursive intersection) type is a pair  $(E, \alpha)$ , where  $E$  is a finite set of equations of the form  $\alpha_i = \sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$ , and  $\sigma$  is of the form  $\bigwedge\{\alpha_1, \dots, \alpha_k\}$ . Here  $m$  and  $k$  may be 0. We use the meta-variable  $\tau$  for recursive intersection types. We write  $\mathbf{Tv}(\tau)$  for the set of type variables occurring in  $\tau$ . A recursive intersection type  $\tau = (E, \alpha)$  is closed if, for every  $\alpha \in \mathbf{Tv}(\tau)$ ,  $(\alpha = \theta) \in E$  for some  $\theta$ . When  $(\alpha = \theta) \in E$ , we write  $E(\alpha)$  for  $\theta$ .

We identify types up to renaming of type variables. For example,  $(\{\alpha = q\}, \alpha)$  is the same as  $(\{\beta = q\}, \beta)$ . Thus, for two closed types  $\tau_0$  and  $\tau_1$ , we always

assume that  $\mathbf{Tv}(\tau_0) \cap \mathbf{Tv}(\tau_1) = \emptyset$ . We often write  $\alpha_1 \wedge \dots \wedge \alpha_k$  or  $\bigwedge_{i \in \{1, \dots, k\}} \alpha_i$  for  $\bigwedge \{\alpha_1, \dots, \alpha_k\}$  and write  $\top$  for  $\bigwedge \emptyset$ . Intuitively,  $(E, \alpha)$  denotes the (recursive) type  $\alpha$  that satisfies the equations in  $E$ . For example,  $(\{\alpha = \alpha \rightarrow q\}, \alpha)$  represents the recursive type  $\mu\alpha.(\alpha \rightarrow q)$  in the usual notation. We often use this term notation for recursive intersection types. By abuse of notation, when  $E(\alpha) = \bigwedge_{i \in I_1} \alpha_i \rightarrow \dots \rightarrow \bigwedge_{i \in I_k} \alpha_i \rightarrow q$ , we write  $\bigwedge_{i \in I_1} (E, \alpha_i) \rightarrow \dots \rightarrow \bigwedge_{i \in I_k} (E, \alpha_i) \rightarrow q$  for  $(E, \alpha)$ . For example, when  $E = \{\alpha_0 = \alpha_1 \rightarrow q_0, \alpha_1 = q_1\}$ ,  $(E, \alpha_0)$  is also written as  $(E, \alpha_1) \rightarrow q_0$  or  $q_1 \rightarrow q_0$ . The type  $\sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$  describes functions that take  $m$  arguments of types  $\sigma_1, \dots, \sigma_m$ , and return a value of type  $q$ . The type  $\alpha_1 \wedge \dots \wedge \alpha_k$  describes values that have all of the types  $\alpha_1, \dots, \alpha_k$ . For example, if  $Q = \{q_1, q_2\}$ , the identity function on base values ( $\lambda x.x$  in the  $\lambda$ -calculus notation) would have types  $(q_1 \rightarrow q_1) \wedge (q_2 \rightarrow q_2)$ .

We define the subtyping relation  $\tau_0 \leq \tau_1$ , which intuitively means, as usual, that any value of type  $\tau_0$  can be used as a value of type  $\tau_1$ .

**Definition 2 (subtyping).** Let  $\tau = (E'', \alpha)$  and  $\tau' = (E', \alpha')$  be closed types, and let  $E = E'' \cup E'$ . The type  $\tau$  is a subtype of  $\tau'$ , written  $\tau \leq \tau'$ , if there exists a binary relation  $\mathcal{R}$  on  $\mathbf{Tv}(\tau) \cup \mathbf{Tv}(\tau')$  such that (i)  $(\alpha, \alpha') \in \mathcal{R}$  and (ii) for every  $(\alpha_0, \alpha'_0) \in \mathcal{R}$ , there exist  $\sigma_1, \dots, \sigma_m, \sigma'_1, \dots, \sigma'_m, q$  such that  $E(\alpha_0) = \sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$  and  $E(\alpha'_0) = \sigma'_1 \rightarrow \dots \rightarrow \sigma'_m \rightarrow q$ , with  $(\sigma'_1, \sigma_1), \dots, (\sigma'_m, \sigma_m) \in \mathcal{R}^\wedge$ . Here,  $\mathcal{R}^\wedge$  is:

$$\{(\bigwedge \{\alpha'_1, \dots, \alpha'_{k'}\}, \bigwedge \{\alpha_1, \dots, \alpha_k\}) \mid \forall i \in \{1, \dots, k\}. \exists j \in \{1, \dots, k'\}. \alpha'_j \mathcal{R} \alpha_i\}.$$

We write  $\tau \cong \tau'$  if  $\tau \leq \tau'$  and  $\tau' \leq \tau$ .

*Example 1.* Let  $\tau_0 = (\{\alpha_0 = \alpha_0 \rightarrow q\}, \alpha_0)$  and  $\tau_1 = (\{\alpha_1 = \alpha_2 \rightarrow q, \alpha_2 = \alpha_1 \wedge \alpha_3 \rightarrow q, \alpha_3 = q\}, \alpha_1)$ .  $\tau_1 \leq \tau_0$  holds, with the relation  $\{(\alpha_1, \alpha_0), (\alpha_0, \alpha_2)\}$  as a witness.

## 2.2 $\mu$ HORS

We introduce below  $\mu$ HORS and its model checking problem, and reduce the latter to a type checking problem. To our knowledge, the notion of  $\mu$ HORS is new, but it is a subclass of the untyped HORS studied by Tsukada and Kobayashi [46], and the reduction from  $\mu$ HORS model checking to type checking (Theorem 2) is a corollary of the result of [46]. We shall therefore quickly go through the definitions and results; more formal definitions (apart from recursive types) and intuitions are found in [33, 22, 46].

**$\mu$ HORS and model checking problems** The set of basic types (called *sorts*) is the subset of recursive intersection types, where  $Q$  is a singleton set  $\{\circ\}$  (where  $\circ$  is the type of trees) and there is no intersection: in  $\sigma = \bigwedge \{\alpha_1, \dots, \alpha_k\}$ ,  $k$  is always 1. Below we often use the following term representation of sorts:

$$\kappa ::= \alpha \mid \kappa_1 \rightarrow \dots \rightarrow \kappa_\ell \rightarrow \circ \mid \mu\alpha.\kappa.$$

Let  $\Sigma$  be a ranked alphabet, i.e., a map from symbols to their arities. An element of  $\text{dom}(\Sigma)$  is used as a tree constructor. A *sort environment* is a map

from variables to sorts. The set of *applicative terms* of type  $\kappa$  under a sort environment  $\mathcal{K}$  is inductively defined by the following rules:

$$\frac{}{\mathcal{K}, x : \kappa \vdash x : \kappa} \qquad \frac{}{\mathcal{K} \vdash a : \underbrace{\circ \rightarrow \cdots \rightarrow \circ}_{\Sigma(a)} \rightarrow \circ}$$

$$\frac{\mathcal{K} \vdash t_1 : \kappa_1 \quad \mathcal{K} \vdash t_2 : \kappa_2 \quad \kappa_1 \cong (\kappa_2 \rightarrow \kappa)}{\mathcal{K} \vdash t_1 t_2 : \kappa}$$

As usual, applications are left-associative, so that  $t_1 t_2 t_3$  means  $(t_1 t_2) t_3$ .

A  $\mu$ HORS  $\mathcal{G}$  is a quadruple  $(\mathcal{N}, \Sigma, \mathcal{R}, S)$  where: (i)  $\mathcal{N}$  is a map from variables (called *non-terminals*) to sorts; (ii)  $\Sigma$  is a ranked alphabet, where  $\text{dom}(\mathcal{N}) \cap \text{dom}(\Sigma) = \emptyset$ ; (iii)  $\mathcal{R}$  is a map from non-terminals to a  $\lambda$ -term of the form  $\lambda x_1. \cdots \lambda x_\ell. t$  where  $t$  is an applicative term; (iv)  $S$ , called the *start symbol*, is a non-terminal such that  $\mathcal{N}(S) = \circ$ . If  $\mathcal{N}(F) = \kappa_1 \rightarrow \cdots \rightarrow \kappa_k \rightarrow \circ$  and  $\mathcal{R}(F) = \lambda x_1. \cdots \lambda x_\ell. t$ , then it must be the case that  $k = \ell$  and  $\mathcal{N}, x_1 : \kappa_1, \dots, x_\ell : \kappa_\ell \vdash t : \circ$ .

The (possibly infinite) tree generated by  $\mathcal{G}$ , written by  $\text{Tree}(\mathcal{G})$ , is defined as the limit of infinite fair reductions of  $S$  [33] where the reduction relation  $\longrightarrow$  is defined by: (i)  $F t_1 \cdots t_\ell \longrightarrow [t_1/x_1, \dots, t_\ell/x_\ell]t$  if  $\mathcal{R}(F) = \lambda x_1. \cdots \lambda x_\ell. t$ ; and (ii)  $a t_1 \cdots t_\ell \longrightarrow a t_1 \cdots t_{i-1} t'_i t_{i+1} \cdots t_\ell$  if  $t_i \longrightarrow t'_i$  for some  $i \in \{1, \dots, \ell\}$ . See [33] for the formal definition of  $\text{Tree}(\mathcal{G})$ .

**Notation 1** We write  $\tilde{u}$  for a sequence  $u_1 \cdots u_\ell$ .  $\lambda \tilde{x}. t$  stands for  $\lambda x_1. \cdots \lambda x_\ell. t$ , and  $[\tilde{s}/\tilde{x}]t$  for  $[s_1/x_1, \dots, s_\ell/x_\ell]t$  (with the understanding that  $\tilde{s}$  and  $\tilde{x}$  have the same length  $\ell$ ). We often write the four components of  $\mathcal{G}$  as  $\mathcal{N}_\mathcal{G}, \Sigma_\mathcal{G}, \mathcal{R}_\mathcal{G}, S_\mathcal{G}$ , and omit the subscript if it is clear from context. We often write  $\mathcal{R}$  as a set of rewriting rules  $\{F_1 x_1 \cdots x_{\ell_1} \rightarrow t_1, \dots, F_m x_1 \cdots x_{\ell_m} \rightarrow t_m\}$  if  $\mathcal{R}(F_i) = \lambda x_1. \cdots \lambda x_{\ell_i}. t_i$  for each  $i \in \{1, \dots, m\}$ .

*Example 2.* Consider  $\mu$ HORS  $\mathcal{G}_1 = (\mathcal{N}_1, \Sigma_1, \mathcal{R}_1, S)$  where

$$\begin{aligned} \mathcal{N}_1 &= \{S \mapsto \circ, F \mapsto (\circ \rightarrow \circ)\} \\ \Sigma_1 &= \{\mathbf{a} \mapsto 2, \mathbf{b} \mapsto 1, \mathbf{c} \mapsto 0\} \\ \mathcal{R}_1 &= \{S \rightarrow F \mathbf{c}, \quad F x \rightarrow \mathbf{a} x (F (\mathbf{b} x))\} \end{aligned}$$

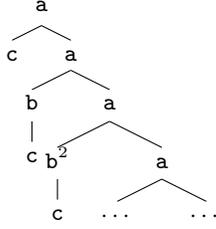
$S$  is rewritten as follows, and the tree in Figure 2 is generated:  
 $S \longrightarrow F \mathbf{c} \longrightarrow \mathbf{a} \mathbf{c} (F (\mathbf{b} \mathbf{c})) \longrightarrow \mathbf{a} \mathbf{c} (\mathbf{a} (\mathbf{b} \mathbf{c}) (F (\mathbf{b} (\mathbf{b} \mathbf{c})))) \longrightarrow \cdots$

*Example 3.* Consider  $\mu$ HORS  $\mathcal{G}_2 = (\mathcal{N}_2, \Sigma_1, \mathcal{R}_2, S)$  where  $\Sigma_1$  is as given in Example 2, and:

$$\begin{aligned} \mathcal{N}_2 &= \{S \mapsto \circ, F \mapsto (\circ \rightarrow \circ), G \mapsto \mu \alpha. (\alpha \rightarrow \circ \rightarrow \circ)\} \\ \mathcal{R}_2 &= \{S \rightarrow F \mathbf{c}, \quad F x \rightarrow G G x, \quad G g x \rightarrow \mathbf{a} x (g g (\mathbf{b} x))\} \end{aligned}$$

This is the same as  $\mathcal{G}_1$  except that recursive types are used instead of term-level recursion.  $S$  is reduced as below, and the same tree as  $\text{Tree}(\mathcal{G}_1)$  is generated.

$$S \longrightarrow F \mathbf{c} \longrightarrow G G \mathbf{c} \longrightarrow \mathbf{a} \mathbf{c} (G G (\mathbf{b} \mathbf{c})) \longrightarrow \mathbf{a} \mathbf{c} (\mathbf{a} (\mathbf{b} \mathbf{c}) (G G (\mathbf{b} (\mathbf{b} \mathbf{c})))) \longrightarrow \cdots$$



**Fig. 2.** The tree generated by  $\mathcal{G}_1$  of Example 2.

*Remark 1.* A tree node that is never instantiated to a terminal symbol is expressed by the special terminal symbol  $\perp$  (with arity 0). For example, for  $\mu\text{HORS}$   $\mathcal{G}_3 = (\mathcal{N}_3, \Sigma_1, \mathcal{R}_3, S)$  where

$$\begin{aligned} \mathcal{N}_3 &= \{S \mapsto \circ, F \mapsto \mu\alpha.(\alpha \rightarrow \circ)\} \\ \mathcal{R}_3 &= \{S \rightarrow FF, Fx \rightarrow xx\}, \end{aligned}$$

$\text{Tree}(\mathcal{G}_3)$  is a singleton tree  $\perp$ . □

As usual [33, 22], we use (top-down) tree automata to express properties of the tree generated by higher-order recursion schemes. For a ranked alphabet  $\Sigma$ , a  $\Sigma$ -labeled tree  $T$  is a map from sequences of natural numbers (which represent paths of the tree) to  $\text{dom}(\Sigma)$ , such that (i) its domain  $\text{dom}(T)$  is non-empty and closed under the prefix operation, and (ii) if  $\pi \in \text{dom}(T)$  then  $\{j \mid \pi j \in \text{dom}(T)\} = \{1, \dots, \Sigma(T(\pi))\}$ . A (deterministic) *trivial automaton* [1]  $\mathcal{B}$  is a quadruple  $(\Sigma, Q, \delta, q_0)$ , where  $\Sigma$  is a ranked alphabet,  $Q$  is a finite set of states,  $\delta$ , called a transition function, is a partial map from  $Q \times \text{dom}(\Sigma)$  to  $Q^*$  such that  $|\delta(q, a)| = \Sigma(a)$ , and  $q_0$  is the initial state. A  $\Sigma$ -labeled tree  $T$  is *accepted* by  $\mathcal{B}$  if there is a  $Q$ -labeled tree  $R$  (called a *run tree*) such that: (i)  $\text{dom}(T) = \text{dom}(R)$ ; (ii)  $R(\epsilon) = q_0$ ; and (iii) for every  $\pi \in \text{dom}(R)$ ,  $\delta(R(\pi), T(\pi)) = R(\pi 1) \cdots R(\pi \Sigma(T(\pi)))$ . For a trivial automaton  $\mathcal{B} = (\Sigma, Q, \delta, q_0)$  (with  $\perp \notin \text{dom}(\Sigma)$ ), we write  $\mathcal{B}^\perp$  for the trivial automaton  $(\Sigma \cup \{\perp \mapsto 0\}, Q, \delta \cup \{(q, \perp) \mapsto \epsilon\} \mid q \in Q, q_0)$ . We often write  $\Sigma_{\mathcal{B}}, Q_{\mathcal{B}}, \delta_{\mathcal{B}}, q_{\mathcal{B}, 0}$  for the four components of  $\mathcal{B}$ , and omit the subscript if it is clear from context. Trivial automata are sufficient for describing safety properties: see [28] for the logical characterization.

*Example 4.* Let  $\mathcal{B}_1 = (\Sigma_1, \{q_0, q_1\}, \delta, q_0)$  where  $\Sigma_1$  is as given in Example 2 and  $\delta$  is given by:

$$\delta(q_0, \mathbf{a}) = q_0 q_0, \quad \delta(q_0, \mathbf{b}) = \delta(q_1, \mathbf{b}) = q_1 \quad \delta(q_0, \mathbf{c}) = \delta(q_1, \mathbf{c}) = \epsilon.$$

It accepts a  $\Sigma_1$ -labeled (ranked) tree  $T$  if and only if  $\mathbf{a}$  does not occur below  $\mathbf{b}$ . In particular,  $\mathcal{B}_1$  accepts the tree shown in Figure 2. □

The  $\mu\text{HORS}$  *model checking* is the problem of checking whether  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^\perp$ , given a  $\mu\text{HORS}$   $\mathcal{G}$  and a trivial automaton  $\mathcal{B}$ .<sup>3</sup> The problem is in general undecidable.

**Theorem 1 ([46]).** *The model checking problem for  $\mu\text{HORS}$  is undecidable.*

[46] gives only a proof sketch. An alternative proof can be obtained by using the reduction from FJ [15] (which is Turing-complete) to  $\mu\text{HORS}$  given in Section 4.1.

**Type system for model checking** We give a sound type system for checking that  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^\perp$ . The set of recursive intersection types is as given in Section 2.1, where the set  $Q$  of base types is the set of states of  $\mathcal{B}$ . Intuitively, a state  $q$  is regarded as the type of trees accepted by  $\mathcal{B}^\perp$  from the state  $q$  [22].

The type judgment relations  $\Gamma \vdash_{\mathcal{B}} t : \tau$  and  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, t) : \tau$  (where  $\Gamma$ , called a type environment, is a set of type bindings of the form  $x : \tau$ ) are defined by:

$$\frac{\tau \leq \tau'}{\Gamma, x : \tau \vdash_{\mathcal{B}} x : \tau'} \quad \frac{\delta_{\mathcal{B}}(q, a) = q_1 \cdots q_k \quad q_1 \rightarrow \cdots \rightarrow q_k \rightarrow q \leq \tau}{\Gamma \vdash_{\mathcal{B}} a : \tau}$$

$$\frac{\Gamma \vdash_{\mathcal{B}} t_1 : \bigwedge_{i \in I} \tau_i \rightarrow \tau \quad \Gamma \vdash_{\mathcal{B}} t_2 : \tau'_i \text{ and } \tau'_i \leq \tau_i \text{ (for every } i \in I)}{\Gamma \vdash_{\mathcal{B}} t_1 t_2 : \tau} \quad \frac{\Gamma, x : \tau_1, \dots, x : \tau_\ell \vdash_{\mathcal{B}} t : \tau \quad x \text{ does not occur in } \Gamma}{\Gamma \vdash_{\mathcal{B}} \lambda x. t : \bigwedge_{i \in \{1, \dots, \ell\}} \tau_i \rightarrow \tau}$$

$$\frac{\forall (F : \tau) \in \Gamma. (\Gamma \vdash_{\mathcal{B}} \mathcal{R}(F) : \tau)}{\vdash_{\mathcal{B}} \mathcal{R} : \Gamma} \quad \frac{\vdash_{\mathcal{B}} \mathcal{R}_{\mathcal{G}} : \Gamma \quad \Gamma \vdash_{\mathcal{B}} t : \tau}{\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, t) : \tau}$$

The following theorem is a special case of the soundness of Tsukada and Kobayashi's infinite intersection type system for untyped HORS [46].

**Theorem 2 (soundness).** *Let  $\mathcal{B}$  be a trivial automaton  $(\Sigma, Q, \delta, q_{\mathcal{B},0})$  and  $\mathcal{G}$  be a  $\mu\text{HORS}$ . If  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$ , then  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^\perp$ .*

*Remark 2.* If  $\mathcal{G}$  does not use recursive sorts, the type system is also complete [22]. The model checking algorithm discussed in this paper is, therefore, complete for the class of  $\mu\text{HORS}$  without recursive sorts.

*Example 5.* Recall  $\mathcal{G}_1$  and  $\mathcal{G}_2$  in Examples 2 and 3, and  $\mathcal{B}_1$  in Example 4.  $\Gamma_1 \vdash_{\mathcal{B}_1} (\mathcal{G}_1, S) : q_0$  and  $\Gamma_2 \vdash_{\mathcal{B}_1} (\mathcal{G}_2, S) : q_0$  hold for:

$$\Gamma_1 = \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0\}$$

$$\Gamma_2 = \Gamma_1 \cup \{G : \mu\alpha. (\alpha \rightarrow (q_0 \wedge q_1) \rightarrow q_0)\} \quad \square$$

Given a type environment  $\Gamma$ , a  $\mu\text{HORS}$   $\mathcal{G}$ , and an automaton  $\mathcal{B}$ , it is decidable whether  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds. Thus,  $\Gamma$  can be used as a certificate for

<sup>3</sup> We consider  $\mathcal{B}^\perp$  instead of  $\mathcal{B}$  as we are interested in safety properties.  $\mathcal{G}_3$  in Remark 1 generates no nodes, hence never violates safety properties; indeed,  $\text{Tree}(\mathcal{G}_3)$  is accepted by  $\mathcal{B}^\perp$  for any  $\mathcal{B}$ .

$Tree(\mathcal{G})$  being accepted by  $\mathcal{B}$ . The converse of the theorem above does not hold, i.e., there is a  $\mu$ HORS  $\mathcal{G}$  such that  $Tree(\mathcal{G})$  is accepted by  $\mathcal{B}^\perp$  but  $Tree(\mathcal{G})$  is not well-typed. To establish the converse, we need to replace (finite) recursive intersection types with infinite intersection types [46]. We have the following properties on the (un)decidability of type checking.

- Theorem 3.** 1. *Given a type environment  $\Gamma$ , a  $\mu$ HORS  $\mathcal{G}$ , and a trivial automaton  $\mathcal{B}$ , it is decidable whether  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds.*
2. *Given a  $\mu$ HORS  $\mathcal{G}$  and a trivial automaton  $\mathcal{B}$ , it is undecidable whether there exists  $\Gamma$  such that  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds. More precisely, the set  $\{\mathcal{G} \mid \exists \Gamma. \Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}\}$  is recursively enumerable but not recursive.*

*Proof.* The first property follows from the fact that the equality and subtyping relations on (finite) recursive intersection types are decidable. To see that  $\{\mathcal{G} \mid \exists \Gamma. \Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}\}$  is recursively enumerable, it suffices to observe that for each  $\mathcal{G}$ , we can enumerate all the possible candidates of  $\Gamma$  and check whether  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds. The set not being recursive follows from the fact that one can encode a Minsky machine [32]  $M$  into a  $\mu$ HORS  $\mathcal{G}_M$ , so that  $M$  halts if and only if  $\mathcal{G}_M$  is typable in the recursive intersection type system. Let  $\mathcal{B}$  be an automaton that accepts the singleton language  $\{\mathbf{end}\}$ . Given a Minsky machine  $M$ , prepare the following non-terminal symbols:

- The start symbol  $S$ , of sort  $\mathbf{o}$ .
- $Zero : \mathbf{N}$ ,  $Succ : \mathbf{N} \rightarrow \mathbf{N}$ ,  $Pred : \mathbf{N} \rightarrow \mathbf{N}$ ,  $Eq : \mathbf{N} \rightarrow \mathbf{N} \rightarrow \mathbf{o} \rightarrow \mathbf{o} \rightarrow \mathbf{o}$  that simulates constructors and operations on natural numbers. Here,  $\mathbf{N}$  is a certain recursive type of terms obtained by encoding natural numbers.
- $F_i$  of sort  $\mathbf{N} \rightarrow \mathbf{N} \rightarrow \mathbf{N} \rightarrow \mathbf{N} \rightarrow \mathbf{o}$ , for each program counter  $i$ .

The first and second arguments of  $F_i$  are the values of two counters. The third and fourth arguments keep the number of execution steps and they are expected to be identical. We prepare the following rules:

- $S \rightarrow F_0 \ Zero \ Zero \ Zero \ Zero$  (assuming that 0 is the initial program counter).
- Appropriate rules for  $Zero$ ,  $Succ$ ,  $Pred$ , and  $Eq$  that simulate the corresponding constructors and operations on natural numbers (this may involve additional non-terminals and rules on them). In particular, the following properties should hold.

$$\begin{aligned} Eq \ t_1 \ t_2 \ t_3 \ t_4 &\longrightarrow_{\mathcal{G}}^* t_3 \text{ if } \llbracket t_1 \rrbracket = \llbracket t_2 \rrbracket = n \text{ for some natural number } n \\ Eq \ t_1 \ t_2 \ t_3 \ t_4 &\longrightarrow_{\mathcal{G}}^* t_4 \text{ if } \llbracket t_1 \rrbracket = m \neq \llbracket t_2 \rrbracket = n \text{ for some natural numbers } m, n \end{aligned}$$

Here,  $\llbracket \cdot \rrbracket$  is a partial function from the set of terms to the set of natural numbers, defined by:

$$\begin{aligned} \llbracket Zero \rrbracket &= 0 & \llbracket Succ(t) \rrbracket &= \llbracket t \rrbracket + 1 \\ \llbracket Pred(t) \rrbracket &= \begin{cases} 0 & \text{if } \llbracket t \rrbracket = 0 \\ n - 1 & \text{if } \llbracket t \rrbracket = n > 0 \\ \text{undefined} & \text{otherwise} \end{cases} \end{aligned}$$

- For each program counter  $i$ , prepare a rule that simulates the instruction  $I_i$  after checking the equivalence of the third and fourth arguments of  $F_i$ . So,
  - If  $I_i$  is “halt,” then

$$F_i c_0 c_1 x y \rightarrow Eq x y \text{ end fail}$$

- If  $I_i$  is “ $c_j := c_j + 1$ ; goto  $k$ ,” then

$$F_i c_0 c_1 x y \rightarrow Eq x y (F_k t_0 t_1 (Succ x) (Succ y)) \text{ fail},$$

where  $t_{j'} = Succ c_j$  if  $j' = j$  and  $t_{j'} = c_{j'}$  otherwise, for  $j' \in \{0, 1\}$ .

- If  $I_i$  is “if  $c_i = 0$  then goto  $k_1$  else  $c_i := c_i - 1$ ; goto  $k_2$ ,” then

$$\begin{aligned} F_i c_0 c_1 x y \rightarrow & Eq x y \\ & (Eq Zero c_i (F_{k_1} c_0 c_1 (Succ x) (Succ y)) \\ & (F_{k_2} t_0 t_1 (Succ x) (Succ y))) \\ & \text{fail} \end{aligned}$$

where  $t_{j'} = Pred c_j$  if  $j' = j$  and  $t_{j'} = c_{j'}$  otherwise, for  $j' \in \{0, 1\}$ .

Let  $\mathcal{G}_M$  be the resulting grammar. We claim that  $M$  halts if and only if  $\mathcal{G}_M$  is typable in the recursive intersection type system. If  $M$  halts, then there is a finite reduction sequence  $S \rightarrow_{\mathcal{G}_M}^* \text{end}$ . By the result on ordinary higher-order model checking [24],  $\mathcal{G}_M$  is typable in the intersection type system even without recursive types. Suppose  $M$  does not halt, but  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  for a finite, recursive type environment  $\Gamma$ . Then, the set  $U$  of applicative terms that have type  $q_{\mathcal{B},0}$  under  $\Gamma$  must be regular (note that each typing rule can be viewed as a transition rule for an alternating tree automaton that accept term trees, having intersection types as states). Furthermore, by the subject reduction property and the condition  $S \in U$ , (i)  $U$  contains all the terms obtained by reducing  $S$ ; and (ii) any element of  $U$  cannot be reduced to **fail**. By condition (ii), if  $F_i t_1 t_2 (Succ^m Zero) (Succ^n Zero) \in U$ , then it must be the case that  $m = n$ . By condition (i) and the fact that  $M$  does not halt, for every  $n$ , there exists  $i, t_1, t_2$  such that  $F_i t_1 t_2 (Succ^n Zero) (Succ^n Zero) \in U$ . But then  $U$  cannot be regular, hence a contradiction.  $\square$

### 3 Model Checking $\mu$ HORS

We now describe the main result of this paper: a model checking procedure for  $\mu$ HORS (the second phase in Figure 1).<sup>4</sup> We shall develop a (possibly non-terminating) procedure CHECK that satisfies:

$$\text{CHECK}(\mathcal{G}, \mathcal{B}) = \begin{cases} \Gamma' \text{ such that } \Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0} & \text{if } \exists \Gamma. \Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0} \\ \text{No (with a counterexample)} & \text{if } \text{Tree}(\mathcal{G}) \text{ is not accepted by } \mathcal{B}^{\perp} \end{cases}$$

<sup>4</sup> Readers who are curious about the relationship between program verification and  $\mu$ HORS may wish to consult Section 4 and then come back to this section.

By Theorem 3, the procedure CHECK can only be a semi-algorithm: it may not terminate if  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^\perp$  but  $\exists \Gamma. \Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  does not hold.

An obvious approach would be to run (i) a sub-procedure  $\text{FINDCERT}(\mathcal{G}, \mathcal{B})$  to enumerate all the finite type environments  $\Gamma$  and output  $\Gamma$  if  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds, and in parallel, (ii) a sub-procedure  $\text{FINDCE}(\mathcal{G}, \mathcal{B})$  to reduce  $\mathcal{G}$  in a fair manner and output No if a partially generated tree is not accepted by  $\mathcal{B}^\perp$ . The first sub-procedure  $\text{FINDCERT}$  is, however, too non-deterministic to be used in practice.

We describe below a more realistic procedure for  $\text{FINDCERT}(\mathcal{G}, \mathcal{B})$  that outputs  $\Gamma$  such that  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  if there is any, and may diverge otherwise. As  $\text{FINDCERT}$  can incrementally find the types of non-terminals, we can use them to improve  $\text{FINDCE}$  as well, by removing well-typed terms from the search space. As such interaction between  $\text{FINDCERT}$  and  $\text{FINDCE}$  is the same as the case without recursive types [20], we focus on the discussion of  $\text{FINDCERT}$  below.

### 3.1 Type Inference Procedure

We first give an informal overview of the idea of  $\text{FINDCERT}$ . Since it is easy to check whether a given  $\Gamma$  is a valid certificate (i.e. whether  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds), the main issue is how to find candidates for  $\Gamma$ . As in the algorithm for HORS without recursive types [20], the idea of finding  $\Gamma$  is to extract type information by partially reducing a given recursion scheme, and observing how each non-terminal symbol is used. For example, suppose that  $S$  is reduced as follows.  $S : q_0 \longrightarrow^* C_1[F G : q_1] \longrightarrow^* C_2[G t : q_2] \longrightarrow^* C_3[t : q_1]$ . Here, we have annotated each term with a state of the property automaton;  $t : q$  means that the tree generated by  $t$  should be accepted from  $q$ . From the reduction sequence, we know  $t$  should have type  $q_1$ , from which we can guess that  $G$  should have type  $q_1 \rightarrow q_2$ , and we can further guess that  $F$  should have type  $(q_1 \rightarrow q_2) \rightarrow q_1$ . This way of guessing types is complete for HORS (without recursive types) [20]. In the presence of recursive types, however, we need a further twist, to obtain (relative) completeness. For example, suppose  $S$  is reduced as follows.  $S : q_0 \longrightarrow^* C_1[F t_1 : q_1] \longrightarrow^* C_2[t_1 t_2 : q_0] \longrightarrow^* C_3[t_2 t_3 : q_1] \longrightarrow^* C_4[t_3 t_4 : q_0] \longrightarrow^* \dots$ . This kind of calling chain terminates for ordinary HORS (since the terms are simply-typed), but may not terminate for  $\mu\text{HORS}$  because of recursive types. (For example, consider a variation of  $\mathcal{G}_3$  in Remark 1, where the rule for  $F$  is replaced by  $F x \rightarrow x (I x)$ , with the new rule  $I x \rightarrow x$ . Then, we have an infinite calling chain:  $S \longrightarrow^* F (I F) \longrightarrow^* (I F) (I (I F)) \longrightarrow^* (I (I F)) (I (I (I F))) \longrightarrow^* \dots$ .) Thus, we would obtain an infinite set of type equations:

$$\alpha_F = \alpha_{t_1} \rightarrow q_1 \quad \alpha_{t_1} = \alpha_{t_2} \rightarrow q_0 \quad \alpha_{t_2} = \alpha_{t_3} \rightarrow q_1 \quad \alpha_{t_3} = \alpha_{t_4} \rightarrow q_0 \quad \dots$$

(where  $\alpha_t$  represents the type of term  $t$ ). To address this problem, we introduce an equivalence relation  $\sim$  on terms, and consider reductions modulo  $\sim$ . In the example above, if we choose  $\sim$  so that  $t_{2n-1} \sim t_{2n+1}$  and  $F \sim t_{2n} \sim t_{2n+2}$ , then we would have finite equations  $\alpha_{[F]} = \alpha_{[t_1]} \rightarrow q_1$  and  $\alpha_{[t_1]} = \alpha_{[F]} \rightarrow q_0$  (where  $[t]$  is the equivalence class containing  $t$ ), from which we can infer  $\mu\alpha.(\alpha \rightarrow q_0) \rightarrow q_1$  as the type of  $F$ . As we show in Theorem 4 later, this way of type inference is complete if a proper equivalence relation  $\sim$  is given as an oracle. It is not

complete in general, but Theorem 5 ensures that no matter how  $\sim$  is chosen, we can “amend” the inferred type environment to obtain a correct type environment. Based on the theorem, we can develop a complete procedure for FINDCERT.

We now turn to describe the idea more formally. Let  $\mathbf{Tm}$  be the set of (well-sorted) closed terms constructed from non-terminals and terminals of  $\mathcal{G}$ , and  $\sim$  be an equivalence relation on  $\mathbf{Tm}$  that induces a *finite* set of equivalence classes. We write  $[t]_{\sim}$  for the equivalence class containing  $t$ , i.e.,  $\{t' \mid t \sim t'\}$ , and omit the subscript if clear from context. Intuitively, the equivalence relation  $t_1 \sim t_2$  means that  $t_1$  and  $t_2$  behave similarly with respect to the given automaton  $\mathcal{B}$ . For the moment, we assume that  $\sim$  is given as an oracle. Throughout the paper, we consider only equivalence relations that equate terms of the same sort, i.e.,  $t \sim t'$  implies  $\mathcal{N} \vdash t : \kappa \iff \mathcal{N} \vdash t' : \kappa$  for every  $\kappa$ .

We define the extended reduction relation  $(\mathcal{X}, \mathcal{U}) \longrightarrow_{\sim} (\mathcal{X}', \mathcal{U}')$  as the least relation closed under the rules below, where  $\mathcal{X}$  is a set of terms and  $\mathcal{U}$  is a set of pairs consisting of a term and an automaton state or a special element `fail`. In rule R-NT,  $\mathbf{STm}(t)$  denotes the set of all subterms of  $t$ .

$$\frac{(a t_1 \cdots t_\ell, q) \in \mathcal{U} \quad \delta(q, a) = q_1 \cdots q_\ell}{(\mathcal{X}, \mathcal{U}) \longrightarrow_{\sim} (\mathcal{X}, \mathcal{U} \cup \{(t_1, q_1), \dots, (t_\ell, q_\ell)\})} \quad (\text{R-CONST})$$

$$\frac{(a t_1 \cdots t_\ell, q) \in \mathcal{U} \quad \delta(q, a) \text{ is undef. or } |\delta(q, a)| \neq \ell}{(\mathcal{X}, \mathcal{U}) \longrightarrow_{\sim} (\mathcal{X}, \mathcal{U} \cup \{\text{fail}\})} \quad (\text{R-F})$$

$$\frac{(F \tilde{t}, q) \in \mathcal{U} \quad \mathcal{R}(F) = \lambda \tilde{x}. u}{(\mathcal{X}, \mathcal{U}) \longrightarrow_{\sim} (\mathcal{X} \cup \mathbf{STm}([\tilde{t}/\tilde{x}]u), \mathcal{U} \cup \{([\tilde{t}/\tilde{x}]u, q)\})} \quad (\text{R-NT})$$

$$\frac{(t t_1 \cdots t_k, q) \in \mathcal{U} \quad t \sim t' \quad t' \in \mathcal{X}}{(\mathcal{X}, \mathcal{U}) \longrightarrow_{\sim} (\mathcal{X}, \mathcal{U} \cup \{(t' t_1 \cdots t_k, q)\})} \quad (\text{R-EQ})$$

The main differences from the reduction relation  $t \longrightarrow t'$  in Section 2.2 are: (i) each term  $t$  (of sort  $\circ$ ) is coupled with its expected type, (ii) such pairs are kept in the  $\mathcal{U}$  component after reductions (in other words,  $(t, q) \in \mathcal{U}$  means that  $t$  should generate a tree accepted by  $\mathcal{B}$  from state  $q$ ), (iii) the  $\mathcal{X}$  component keeps all the sub-terms that have occurred so far, and (iv) a subterm in a head position can be replaced by another term belonging to the same equivalence class (see rule R-EQ above). In rule R-CONST,  $(a t_1 \cdots t_\ell, q)$  being an element of  $\mathcal{U}$  means that  $a t_1 \cdots t_\ell$  should generate a tree of type  $q$  (i.e., should be accepted by  $\mathcal{B}$  from the state  $q$ ). The premise  $\delta(q, a) = q_1 \cdots q_\ell$  means that the  $i$ -th subtree should have type  $q_i$ , so that we add  $(t_i, q_i)$  to the second component. Rule R-F is applied when  $(a t_1 \cdots t_\ell, q)$  is in the second set but no tree having  $a$  as its root can be accepted from the state  $q$ . The condition  $|\delta(q, a)| \neq \ell$  actually never holds, by the assumption that  $\sim$  equates only terms of the same sort. R-NT is the rule for reducing non-terminals. As mentioned above, rule R-EQ is used to replace a head of a term with an equivalent term with respect to  $\sim$ . Extended reduction sequences are in general infinite, and non-deterministic.

*Example 6.* Recall  $\mathcal{G}_2$  in Example 3. Let  $\sim^{(1)}$  be the least congruence relation that satisfies  $\mathbf{b}(c) \sim c$ . Then, by using  $\sim^{(1)}$  as  $\sim$ , we can reduce  $(\{S\}, \{(S, q_0)\})$  as follows:

$$(S, q_0) \rightarrow (F c, q_0) \rightarrow (G G c, q_0) \rightarrow (a c (G G (b c)), q_0) \rightarrow (G G (b c), q_0) \\ \leftarrow (c, q_0) \quad \leftarrow (c, q_1) \quad \leftarrow (b c, q_0) \quad \leftarrow (a (b c) (G G (b (b c))), q_0) \rightarrow \dots$$

Here, we have omitted the  $\mathcal{X}$ -component, and shown only elements relevant to reductions instead of the whole  $\mathcal{U}$ -component. In the figure, dashed arrows represent reductions by using rule R-EQ, and solid arrows represent reductions obtained by the other rules. The figure shows a state obtained by a finite number of reductions. From an infinite fair reduction sequence, we obtain the following set as  $\mathcal{U}$ :

$$\{(F(\mathbf{b}^k c), q_0), (G G(\mathbf{b}^k c), q_0), (\mathbf{b}^k c, q_0), (\mathbf{b}^k c, q_1) \mid k \geq 0\} \\ \cup \{(S, q_0)\} \cup \{(a(\mathbf{b}^k c)(G G(\mathbf{b}^\ell c)), q_0) \mid k, \ell \geq 0\} \quad \square$$

The goal below is to construct a candidate of type environment  $\Gamma$  that satisfies  $\Gamma \vdash_{\mathcal{B}} \mathcal{G} : q_0$ , from a fair reduction sequence (where a reduction sequence is fair if every enabled reduction is eventually reduced). The idea of the construction of  $\Gamma$  is similar to the case for ordinary HORS [20]. For example, in Example 6 above, from the pairs  $(c, q_0)$  and  $(c, q_1)$ , we can guess that the type of  $c$  is  $q_0 \wedge q_1$ . From the pair  $(F c, q_0)$ , we guess that the return type of  $F$  is  $q_0$ , so that the type of  $F$  is  $q_0 \wedge q_1 \rightarrow q_0$ . The actual construction is, however, more involved than [20] because of the presence of recursive types and the term equivalence relation  $\sim$ .

Let  $(\mathcal{X}_0, \mathcal{U}_0) \rightarrow_{\sim} (\mathcal{X}_1, \mathcal{U}_1) \rightarrow_{\sim} (\mathcal{X}_2, \mathcal{U}_2) \rightarrow_{\sim} \dots$  be a fair reduction sequence where  $\mathcal{X}_0 = \{S\}$  and  $\mathcal{U}_0 = \{(S, q_0)\}$ , and let  $\mathcal{X}$  and  $\mathcal{U}$  be  $\bigcup_{i \in \omega} \mathcal{X}_i$  and  $\bigcup_{i \in \omega} \mathcal{U}_i$  respectively. We prepare a type variable  $\alpha_{[t_0], \dots, [t_k], q}$  for each  $(t_0 t_1 \dots t_k, q) \in \mathcal{U}$ . Intuitively,  $\alpha_{[t_0], \dots, [t_k], q}$  is the type of  $t_0$  in  $t_0 t_1 \dots t_k : q$ . Let  $E$  be:

$$\{\alpha_{[t_0], [t_1], \dots, [t_k], q} = \sigma_{[t_1]} \rightarrow \dots \rightarrow \sigma_{[t_k]} \rightarrow q \mid (t_0 t_1 \dots t_k, q) \in \mathcal{U}\},$$

where  $\sigma_{[t]} = \bigwedge \{\alpha_{[t], [t'_1], \dots, [t'_\ell], q'} \mid (t t'_1 \dots t'_\ell, q') \in \mathcal{U}\}$ . We define the type environment  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  as  $\{F : (E, \alpha_{[F], [t_1], \dots, [t_k], q}) \mid (F t_1 \dots t_k, q) \in \mathcal{U}\}$ . By the condition that  $\sim$  induces a *finite* number of equivalence classes,  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  is finite.

By the definition above, we have:

**Lemma 1.** *If the number of equivalence classes induced by  $\sim$  is finite, then  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  is also finite. Furthermore, there exists  $m$  such that  $\Gamma_{\mathcal{X}_m, \mathcal{U}_m, \sim} = \Gamma_{\mathcal{X}, \mathcal{U}, \sim}$ .*

Although  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  above is defined for a fair reduction sequence that is usually *infinite*, Lemma 1 above says that  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  can be computed from a *finite* prefix of the fair reduction sequence.

*Example 7.* From the reductions in Example 6, we get the following type equations:

$$\alpha_{S, q_0} = q_0 \quad \alpha_{F, c, q_0} = \alpha_{c, q_0} \wedge \alpha_{c, q_1} \rightarrow q_0 \quad \alpha_{c, q_0} = q_0 \quad \alpha_{c, q_1} = q_1 \\ \alpha_{G, G, c, q_0} = \alpha_{G, G, c, q_0} \rightarrow \alpha_{c, q_0} \wedge \alpha_{c, q_1} \rightarrow q_0$$

Thus, the extracted type environment (in the usual term representation) is:

$$\{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \mu\alpha.(\alpha \rightarrow (q_0 \wedge q_1) \rightarrow q_0)\}. \quad \square$$

Even if  $\mathcal{G}$  is typable,  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  may not be a proper witness, i.e.,  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B}, 0}$  may not hold in general. The theorem below (Theorem 4), however, ensures that if  $\mathcal{G}$  is typable and if  $\sim$  is properly chosen,  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  is indeed a proper witness. For a type environment  $\Gamma$ , we define the equivalence relation  $\sim_{\Gamma}$  by:  $\sim_{\Gamma} = \{(t_1, t_2) \mid \forall \tau. (\Gamma \vdash t_1 : \tau \iff \Gamma \vdash t_2 : \tau)\}$ .

**Theorem 4.** *If  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B}, 0}$  and  $\sim \subseteq \sim_{\Gamma}$ , then  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B}, 0}$ .*

*Proof.* See Appendix A.1.

*Example 8.* Recall  $\mathcal{G}_2$  in Example 3, and  $\Gamma_2 = \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \mu\alpha.(\alpha \rightarrow (q_0 \wedge q_1) \rightarrow q_0)\}$  in Example 5.  $\sim_{\Gamma_2}$  is a congruence that satisfies  $\mathbf{bc} \sim_{\Gamma_2} \mathbf{c}$ ,  $S \sim_{\Gamma_2} F \mathbf{c}$ , and  $F \sim_{\Gamma_2} G G$ . The relation  $\sim$  in Example 6 satisfies the assumption  $\sim \subseteq \sim_{\Gamma_2}$  of Theorem 4, and  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}_1} (\mathcal{G}_2, S) : q_0$  holds indeed.

Theorem 4 cannot be directly used for type inference, since we do not know  $\sim_{\Gamma}$  in advance. We shall prove below (in Theorem 5) that even if  $\sim$  is not a subset of  $\sim_{\Gamma}$ , we can “amend” the type environment to get a valid one, by using the refinement relation  $\sqsubseteq$  below. Intuitively,  $\tau_1 \sqsubseteq \tau_2$  means that  $\tau_1$  is obtained from  $\tau_2$  by removing some intersection types. Note that unlike subtyping, the refinement relation is co-variant in the function type constructor ( $\rightarrow$ ).

**Definition 3 (refinement).** *Let  $\tau_0 = (E_0, \alpha_0)$  and  $\tau_1 = (E_1, \alpha_1)$  be closed types, and let  $E = E_0 \cup E_1$ . The type  $\tau_0$  is a refinement of  $\tau_1$ , written  $\tau_0 \sqsubseteq \tau_1$ , if there exists a binary relation  $\mathcal{R}$  on  $\mathbf{Tv}(\tau_1) \cup \mathbf{Tv}(\tau_2)$  such that (i)  $(\tau_0, \tau_1) \in \mathcal{R}$  and (ii) for every  $(\tau'_0, \tau'_1) \in \mathcal{R}$ , there exist  $\sigma_1, \dots, \sigma_m, \sigma'_1, \dots, \sigma'_m, q$  such that  $E(\alpha'_0) = \sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$  and  $E(\alpha'_1) = \sigma'_1 \rightarrow \dots \rightarrow \sigma'_m \rightarrow q$ , with  $(\sigma_1, \sigma'_1), \dots, (\sigma_m, \sigma'_m) \in \mathcal{R}^{\sqsubseteq}$ . Here,  $\mathcal{R}^{\sqsubseteq}$  is defined as:*

$$\{(\bigwedge\{\alpha_1, \dots, \alpha_k\}, \bigwedge\{\alpha'_1, \dots, \alpha'_k\}) \mid \forall i \in \{1, \dots, k\}. \exists j \in \{1, \dots, k'\}. \alpha_i \mathcal{R} \alpha'_j\}.$$

We write  $\Gamma_1 \sqsubseteq \Gamma_2$  if  $\text{dom}(\Gamma_1) \subseteq \text{dom}(\Gamma_2)$  and for every  $x : \tau_1 \in \Gamma_1$ , there exists  $\tau_2$  such that  $x : \tau_2 \in \Gamma_2$  and  $\tau_1 \sqsubseteq \tau_2$ .

*Example 9.* Let  $\tau_1$  be  $q_1 \rightarrow q_2$  and  $\tau_2$  be  $(q_1 \wedge q_0) \rightarrow q_2$ . Then  $\tau_1 \sqsubseteq \tau_2$  and  $\tau_1 \rightarrow q_0 \sqsubseteq \tau_2 \rightarrow q_0$  hold. Note that  $\tau_1 \leq \tau_2$  but  $\tau_1 \rightarrow q_0 \not\leq \tau_2 \rightarrow q_0$ . For  $\tau_0$  and  $\tau_1$  in Example 1,  $\tau_0 \sqsubseteq \tau_1$  but  $\tau_1 \not\sqsubseteq \tau_0$ .  $\mathcal{R}_2 = \{(\tau_0, \tau_1), (\tau_0, \tau_2)\}$  is a witness for  $\tau_0 \sqsubseteq \tau_1$ .  $\square$

**Theorem 5.** *Suppose  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B}, 0}$ . Let  $\sim$  be an equivalence relation on  $\mathbf{Tm}$  and  $(\mathcal{X}_0, \mathcal{U}_0) \xrightarrow{\sim} (\mathcal{X}_1, \mathcal{U}_1) \xrightarrow{\sim} (\mathcal{X}_2, \mathcal{U}_2) \xrightarrow{\sim} \dots$  be a fair reduction sequence, with  $(\mathcal{X}_0, \mathcal{U}_0) = (\{S\}, \{(S, q_{\mathcal{B}, 0})\})$ . Let  $\mathcal{U} = \bigcup_i \mathcal{U}_i$  and  $\mathcal{X} = \bigcup_i \mathcal{X}_i$ . Then, there exists  $\Gamma'$  such that  $\Gamma' \sqsubseteq \Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  and  $\Gamma' \vdash (\mathcal{G}, S) : q_{\mathcal{B}, 0}$ ,*

The proof is given in Appendix A. Intuitively, Theorem 5 holds because, if  $\sim$  is not a subset of  $\sim_{\Gamma}$ , we only get extra reduction sequences, whose effect is only to add extra type bindings and elements in intersections. Thus, by removing the extra nodes and edges (using the refinement relation from right to left), we can obtain a proper type environment.



```

FINDCERT( $\mathcal{G}, \mathcal{B}$ ) = Rep( $\mathcal{G}, \mathcal{B}, \{S\}, \{(S, q_{\mathcal{B},0})\}, \{(S, S)\}, 1$ )
Rep( $\mathcal{G}, \mathcal{B}, \mathcal{X}, \mathcal{U}, \sim, v$ ) =
  let ( $\mathcal{X}, \mathcal{U}$ )  $\xrightarrow{\ell}$  ( $\mathcal{X}', \mathcal{U}'$ ) in let  $\sim' = \text{expandEq}(\sim, \mathcal{X}')$  in
    if  $\Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  for some  $\Gamma' \sqsubseteq \Gamma_{\mathcal{X}', \mathcal{U}', \sim'}$  and  $|\Gamma'| \leq v$  then return  $\Gamma'$ 
    else Rep( $\mathcal{G}, \mathcal{B}, \mathcal{X}', \mathcal{U}', \sim', v+1$ )

```

**Fig. 4.** A type inference procedure. ( $|\Gamma|$  denotes the largest type size in  $\Gamma$ .)

We first rephrase the condition  $|\Gamma'| \leq v \wedge \Gamma' \sqsubseteq \Gamma$ . For a set  $E = \{\alpha_1 = \sigma_{1,1} \rightarrow \dots \sigma_{1,m_1} \rightarrow q_1, \dots, \alpha_n = \sigma_{n,1} \rightarrow \dots \sigma_{n,m_n} \rightarrow q_n\}$ , we write  $E^{(k)}$  for:

$$\begin{aligned} \{\alpha_1^{(1)} = \sigma_{1,1}^{(k)} \rightarrow \dots \sigma_{1,m_1}^{(k)} \rightarrow q_1, \dots, \alpha_n^{(1)} = \sigma_{n,1}^{(k)} \rightarrow \dots \sigma_{n,m_n}^{(k)} \rightarrow q_n, \dots, \\ \alpha_1^{(k)} = \sigma_{1,1}^{(k)} \rightarrow \dots \sigma_{1,m_1}^{(k)} \rightarrow q_1, \dots, \alpha_n^{(k)} = \sigma_{n,1}^{(k)} \rightarrow \dots \sigma_{n,m_n}^{(k)} \rightarrow q_n, \dots\}, \end{aligned}$$

obtained by preparing  $k$  copies for each type variable. Here, for  $\sigma = \bigwedge\{\alpha_1, \dots, \alpha_\ell\}$ ,  $\sigma^{(k)}$  represents  $\bigwedge\{\alpha_1^{(1)}, \dots, \alpha_\ell^{(1)}, \dots, \alpha_1^{(k)}, \dots, \alpha_\ell^{(k)}\}$ . Clearly,  $(E, \alpha_i) \cong (E^{(k)}, \alpha_i^{(1)})$ . We write  $\Gamma^{(k)}$  for  $\{x : (E^{(k)}, \alpha^{(i)}) \mid x : (E, \alpha) \in \Gamma, 1 \leq i \leq k\}$ .

We write  $E \sqsubseteq_{\mathbf{s}} E'$  if  $E$  is obtained from  $E'$  by removing some elements from intersections, i.e., if  $E = \{\alpha_1 = \bigwedge S_{1,1} \rightarrow \dots \bigwedge S_{1,m_1} \rightarrow q_1, \dots, \alpha_n = \bigwedge S_{n,1} \rightarrow \dots \bigwedge S_{n,m_n} \rightarrow q_n\}$  and  $E' = \{\alpha_1 = \bigwedge S'_{1,1} \rightarrow \dots \bigwedge S'_{1,m_1} \rightarrow q_1, \dots, \alpha_n = \bigwedge S'_{n,1} \rightarrow \dots \bigwedge S'_{n,m_n} \rightarrow q_n\}$  with  $S_{i,j} \subseteq S'_{i,j}$  for every  $i, j$ . It is pointwise extended to  $\Gamma \sqsubseteq_{\mathbf{s}} \Gamma'$  by:  $\Gamma \sqsubseteq_{\mathbf{s}} \Gamma' \iff \forall x : (E, \alpha) \in \Gamma, \exists x : (E', \alpha) \in \Gamma'. E \sqsubseteq_{\mathbf{s}} E'$ . Then,  $\Gamma' \sqsubseteq \Gamma$  is equivalent to  $\exists k. \Gamma' \sqsubseteq_{\mathbf{s}} \Gamma^{(k)}$  (up to renaming of type variables). Thus, the condition  $|\Gamma'| \leq v \wedge \Gamma' \sqsubseteq \Gamma$  in the algorithm can be replaced by  $\Gamma' \sqsubseteq_{\mathbf{s}} \Gamma^{(v)}$  without losing completeness.

To check whether there exists  $\Gamma'$  such that  $\Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  and  $\Gamma' \sqsubseteq_{\mathbf{s}} \Gamma^{(k)}$ , we attach a boolean variable to each type binding and each element of an intersection in  $\Gamma^{(k)}$ , to express whether  $\Gamma'$  has the corresponding binding or element. Thus, an annotated type environment is of the form  $\{x_1 :^{b_1} \tau_1, \dots, x_m :^{b_m} \tau_m\}$ , where each type equation in  $\tau_1, \dots, \tau_m$  is now of the form:  $\alpha = \bigwedge_{i \in I_1} b_{1,i} \alpha_{1,i} \rightarrow \dots \rightarrow \bigwedge_{k \in I_k} b_{k,i} \alpha_{k,i} \rightarrow q$ . Given an assignment function  $f$  for boolean variables, the type environment  $f(\Delta)$  is given by:

$$\begin{aligned} f(\Delta) &= \{x_i : f(\rho_i) \mid x_i :^{b_i} \rho_i \in \Delta \wedge f(b_i) = \mathbf{true}\} \\ f(E, \alpha) &= (\{\alpha = f(\xi_1) \rightarrow \dots \rightarrow f(\xi_k) \rightarrow q \mid (\alpha = \xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q) \in E\}, \alpha) \\ f(\bigwedge_{i \in I} b_i \alpha_i) &= \bigwedge\{\alpha_i \mid i \in I, f(b_i) = \mathbf{true}\} \end{aligned}$$

Let  $\Delta$  be the type environment obtained by attaching boolean variables to  $\Gamma^{(k)}$ . Then, the condition  $\Gamma' \sqsubseteq_{\mathbf{s}} \Gamma^{(k)} \wedge \Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  is reduced to: “Is there a boolean assignment  $f$  such that  $f(\Delta) \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$ ?” It can be expressed as a SAT problem as follows. We first introduce additional boolean variables: (i) For each rule  $F \mapsto \lambda x_1. \dots \lambda x_k. t \in \mathcal{R}$ , a subterm  $s$  of  $t$ , a type binding  $F :^b \xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \in \Delta$ , and a type  $\rho$  in  $\Delta$ , we prepare a variable  $b_{\Delta, x_1 : \xi_1, \dots, x_k : \xi_k \vdash s : \rho}$ , which expresses whether  $f(\Delta, x_1 : \xi_1, \dots, x_k : \xi_k) \vdash_{\mathcal{B}} s : f(\rho)$  should hold. (ii) For

each pair  $(\rho_1, \rho_2)$  of types occurring in  $\Delta$ , we introduce  $b_{\rho_1 \leq \rho_2}$ , which expresses whether  $f(\rho_1) \leq f(\rho_2)$  should hold. Now, the existence of a boolean assignment function  $f$  such that  $f(\Delta) \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  is reduced to the satisfiability of the conjunction of all the following boolean formulas. We write  $F : \bigwedge_{j \in \{1..n\}} b_j \rho_j \in \Delta$  for  $F :^{b_1} \rho_1, \dots, F :^{b_n} \rho_n \in \Delta$  below. For simplicity, we omit type equations  $E$  and identify  $\alpha$  and  $E(\alpha)$  below.

- (i)  $\bigvee \{b_i \mid S :^{b_i} q_{\mathcal{B},0} \in \Delta\}$ .
- (ii)  $b \Rightarrow b_{\Delta, x_1 : \xi_1, \dots, x_k : \xi_k \vdash t : q}$   
for each  $F :^b \xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \in \Delta$  such that  $\mathcal{R}(F) = \lambda x_1, \dots, x_k. t$ .
- (iii)  $b_{\Delta', x : \bigwedge_{j \in J} b_j \rho_j \vdash x : \rho} \Rightarrow \bigvee_{j \in J} (b_j \wedge b_{\rho_j \leq \rho})$ , for each  $b_{\Delta', x : \bigwedge_{j \in J} b_j \rho_j \vdash x : \rho}$ .
- (iv)  $b_{\Delta' \vdash a : \rho} \Rightarrow \bigvee \{b_{q_1 \rightarrow \dots \rightarrow q_k \rightarrow q \leq \rho} \mid \delta(a, q) = q_1 \cdots q_k\}$ , for each  $b_{\Delta' \vdash a : \rho}$ .
- (v)  $b_{\Delta' \vdash t_1 t_2 : \rho} \Rightarrow \bigvee (b_{\Delta' \vdash t_1 : (\bigwedge_{j \in J} b_j \rho_j) \rightarrow \rho} \wedge (\bigwedge_{j \in J} (b_j \Rightarrow \bigvee (b_{\Delta' \vdash t_2 : \rho'} \wedge b_{\rho' \leq \rho_j}))))$ ,  
for each  $b_{\Delta' \vdash t_1 t_2 : \rho}$ .
- (vi)  $b_{(\bigwedge_{i \in I} b_i \rho_i) \leq (\bigwedge_{j \in J} b_j \rho'_j)} \Rightarrow \bigwedge_{j \in J} (b_j \Rightarrow \bigvee_{i \in I} (b_i \wedge b_{\rho_i \leq \rho'_j}))$ ,  
for each  $b_{(\bigwedge_{i \in I} b_i \rho_i) \leq (\bigwedge_{j \in J} b_j \rho'_j)}$ .
- (vii)  $b_{\xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \leq \xi'_1 \rightarrow \dots \rightarrow \xi'_m \rightarrow q'} \Rightarrow k = m \wedge q = q' \wedge \bigwedge_{i \in \{1, \dots, k\}} b_{\xi'_i \leq \xi_i}$ ,  
for each  $b_{\xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \leq \xi'_1 \rightarrow \dots \rightarrow \xi'_m \rightarrow q'}$ .

The first condition (i) ensures that  $S : q_{\mathcal{B},0} \in f(\Delta)$ . The condition (ii) ensures that each type binding in  $f(\Delta)$  is valid (i.e.,  $\vdash_{\mathcal{B}} \mathcal{R} : f(\Delta)$ ). The next three conditions (iii)-(v) express the validity of a type judgment  $f(\Delta, x_1 : \xi_1, \dots, x_k : \xi_k) \vdash_{\mathcal{B}} t : f(\rho)$ , corresponding to the typing rules for variables, constants, and applications. The last two conditions express the validity of a subtype relation.

By the above construction, there exists a boolean assignment function  $f$  such that  $f(\Delta) \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  if and only if the conjunction of the above boolean formulas is satisfiable. The latter can be solved by using a SAT solver.

*Example 11.* Recall  $\Gamma_{\mathcal{X}^{(2)}, \mathcal{U}^{(2)}, \sim^{(2)}}$  in Example 10:

$$\Gamma_{\mathcal{X}^{(2)}, \mathcal{U}^{(2)}, \sim^{(2)}} = \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \tau_0, G : \tau_1\},$$

where  $\tau_i = (\tau_1 \wedge \tau_2) \rightarrow (q_0 \wedge q_1) \rightarrow q_i$  for  $i \in \{0, 1\}$ . To find a type environment  $\Gamma$  such that  $\Gamma \sqsubseteq \Gamma_{\mathcal{X}^{(2)}, \mathcal{U}^{(2)}, \sim^{(2)}}$  and  $\Gamma \vdash_{\mathcal{B}_1} (\mathcal{G}_2, S) : q_0$ , let us add boolean variables to type bindings and types as follows:

$$\begin{aligned} \Delta &= \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G :^{b_0} \alpha_0, G :^{b_1} \alpha_1\} \\ \alpha_i &= (b_2 \alpha_0 \wedge b_3 \alpha_1) \rightarrow (q_0 \wedge q_1) \rightarrow q_i \quad (i \in \{0, 1\}) \end{aligned}$$

(Here, for the sake of simplicity, we have added boolean variables only to critical parts, assuming that the values of other boolean variables are true; we also apply various simplifications below.) From the typing of  $G$ , we get the following boolean constraints:

$$\begin{aligned} b_i &\Rightarrow b_{\Delta' \vdash \mathbf{a} x (g g (\mathbf{b} x)) : q_i} \quad (\text{for } i \in \{0, 1\}) & b_{\Delta' \vdash \mathbf{a} x (g g (\mathbf{b} x)) : q_0} &\Rightarrow b_{\Delta' \vdash g g : q_0 \wedge q_1 \rightarrow q_0} \\ b_{\Delta' \vdash \mathbf{a} x (g g (\mathbf{b} x)) : q_1} &\Rightarrow \mathbf{false} & b_{\Delta' \vdash g : \tau_0} &\Rightarrow b_2 & b_{\Delta' \vdash g : \tau_1} &\Rightarrow b_3 \\ b_{\Delta' \vdash g g : q_0 \wedge q_1 \rightarrow q_0} &\Rightarrow (b_{\Delta' \vdash g : \tau_0} \wedge (b_2 \Rightarrow b_{\Delta' \vdash g : \tau_0})) \wedge (b_3 \Rightarrow b_{\Delta' \vdash g : \tau_1}) \end{aligned}$$

Here,  $\Delta' = \Delta \cup \{g :^{b_2} \tau_0, g :^{b_3} \tau_1, x : q_0, x : q_1\}$ . The above conditions are satisfied by  $f$  such that  $f(b_0) = f(b_2) = \mathbf{true}$  and  $f(b_1) = f(b_3) = \mathbf{false}$ . Thus, we get

$$\begin{aligned} \Gamma' = f(\Delta) &= \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \tau_0\} \\ \tau_0 &= \tau_0 \rightarrow (q_0 \wedge q_1) \rightarrow q_0. \end{aligned}$$

We have  $\Gamma' \vdash_{\mathcal{B}_1} (\mathcal{G}_2, S) : q_0$  as required. □

## 4 Applications

This section discusses two applications of  $\mu\text{HORS}$  model checking: verification of (functional) object-oriented programs and that of higher-order multi-threaded programs. Those programs can be verified via reduction to  $\mu\text{HORS}$  model checking. In both applications, the translation from a source program to  $\mu\text{HORS}$  is just like giving the semantics of the source program (in terms of the  $\lambda$ -calculus). This comes from the expressive power of the model of  $\mu\text{HORS}$  model checking (i.e.,  $\mu\text{HORS}$ ), which is the main advantage of our approach.

### 4.1 Model-Checking Functional Objects

In this section, we discuss how to reduce verification problems for (functional) object-oriented programs written in (a call-by-value variant of) Featherweight Java (FJ) [15] to  $\mu\text{HORS}$  model checking problems. The idea is to transform an FJ source program into  $\mu\text{HORS}$  that generates a tree representing all the possible action sequences of the source program. The translation is sound *and complete* in the sense that all *and only* action sequences that occur are represented in the tree. Properties that can be checked include:

- reachability, that is, whether program execution reaches certain program points;
- order of method invocations; and
- whether downcasts may fail.

After we introduce the source language FJ-- briefly, we give a formal translation from FJ-- to  $\mu\text{HORS}$  and discuss how the properties above can be model-checked.

**FJ--** We first describe main differences of FJ-- from FJ. In FJ--, there is a construct to signal certain actions, which correspond to terminal symbols in  $\mu\text{HORS}$ . So, (multi-step) reduction relation is augmented with a sequence of actions that occur during reduction. We also add a non-deterministic choice operator, which will be necessary when we apply predicate abstractions [29].

To ease presentation, FJ-- assumes a few simplifications:

- Methods are called by value.
- Field access is allowed only in the form `this.f`.

- For each method name, its arity (the number of arguments) is globally fixed.

Not only is the language call-by-value, but also the order of evaluation of subexpressions is fixed. The second restriction is not essential because we can add getter methods and replace any field access to an invocation of a corresponding getter method. The third restriction is an artifact of our translation but does not weaken the expressive power of the language.

Now we briefly introduce FJ--. As usual, we assume class names, ranged over by  $C, D, \dots$ , field names, ranged over by  $f, g$ , method names, ranged over by  $m$ . Let the set of action names, ranged over by  $a$ , be *Actions*. The syntax of FJ-- is given as follows:

$$\begin{aligned}
L &::= \text{class } C \text{ extends } C \{ \widetilde{C} \widetilde{f}; K \widetilde{M} \} && \text{(classes)} \\
K &::= C(\widetilde{C} \widetilde{f}) \{ \text{super}(\widetilde{f}); \text{this.f} = \widetilde{f}; \} && \text{(constructors)} \\
M &::= C m(\widetilde{C} \widetilde{x}) \{ s \} && \text{(methods)} \\
v &::= x \mid \text{this.f} \mid \text{new } C(\widetilde{v}) && \text{(values)} \\
s &::= \text{return } v; \mid C x = v_0.m(\widetilde{v}); s && \text{(statements)} \\
&\mid a; s \mid s_1 \square s_2 \mid \text{fail};
\end{aligned}$$

Unlike the original definition of FJ, a method body is required to be written in A-normal form [10] to make the definition of the translation more concise. The statement of the form  $C x = v_0.m(\widetilde{v}); s$  first invokes method  $m$  on  $v_0$ , bind  $x$  to the result, and executes  $s$ . The statement of the form  $a; s$  signals  $a$  and executes  $s$ . The statement  $s_1 \square s_2$  executes  $s_1$  or  $s_2$  in a non-deterministic manner. The statement *fail*; causes abnormal termination of the execution. We omit downcasts from FJ-- but it is easy to add (see also Section 4.1). We denote the set of free variables in a statement  $s$  by  $FV(s)$ . An FJ-- program is a pair  $(CT, s)$  of a class table (a mapping from class names to classes) and a statement, which corresponds to the body of the `main` method. We assume the same sanity conditions on the class table as in FJ.

The definitions of auxiliary functions  $fields(C)$ ,  $mtype(m, C)$ ,  $mbody(m, C)$  to look up fields, method types, and method bodies are essentially the same as those of FJ and so omitted. The type system of FJ-- is a straightforward adaptation of that of FJ and so we omit typing rules but write  $\vdash (CT, s) : C$  to mean that  $CT$  is a well-formed class table and  $s$  is given type  $C$  under  $CT$ . Operational semantics is given by a multi-step reduction relation written  $s \xrightarrow{a_1 \dots a_n} r$ , where  $r$  is either a statement  $s'$  or a special symbol  $\bullet$ , which means termination. The relation means that statement  $s$  is reduced to  $r$  in multiple steps and actions  $a_1, \dots, a_n$  occur during reduction in this order. When  $r$  is  $\bullet$ , the last action is either `e`, which stands for normal termination by `returning` a value, or `fail` for abnormal termination caused by *fail* or `NoSuchMethodError`. We also omit reduction rules, which are also straightforward.

We use the following classes that represent natural numbers with methods for addition (`add`) and predecessors (`pred`) as a running example. Method `rand` non-deterministically returns a natural number that is equal to or greater than the argument. The main expression to be executed takes a predecessor of a (non-deterministically chosen) non-zero natural number.

```

class Nat extends Object {
  Nat add(Nat n) { fail; }
  Nat pred() { fail; }
  Nat rand(Nat n) {
    return n; □ return new S(this.rand(n));
  }
}
class Z extends Nat { // Zero
  Nat add(Nat n) { return n; }
}
class S extends Nat { // Successor
  Nat p;
  Nat add(Nat n) {
    Nat p' = this.p.add(n);
    return new S(p');
  }
  Nat pred() { return this.p; }
}
// main expression
new Z().rand(new Z()).add(new S(new Z())).pred();

```

To verify program execution does not `fail`, we translate the program to a  $\mu$ HORS that generates the tree representing all the possible global events, like:

$$\begin{array}{c}
 \text{br} \\
 \widehat{\text{e br}} \\
 \widehat{\text{e ..}}
 \end{array}$$

Here, `br` and `e` represent non-deterministic branch (caused by `□`) and program termination, respectively. Then, it suffices to check that the tree does not contain `fail` by using  $\mu$ HORS model checking.

**Translation to  $\mu$ HORS** The main ideas of translation are: (i) to express an object as a record (or tuple) of functions that represent methods [7], and (ii) to represent each method in the continuation passing style (CPS) in order to correctly reflect the evaluation order and action sequences to  $\mu$ HORS. For example, an object of class `S` is expressed by a tuple  $\langle S\_add, S\_pred, S\_rand \rangle$  of functions  $S\_add$ ,  $S\_pred$  and  $S\_rand$  that represent methods `add`, `pred`, and `rand` defined or inherited in class `S`, respectively. A function that represents a method takes an argument that represents “self” and a continuation argument, as well as ordinary arguments of the method. In general, a method of the form

$$C_0 \ m(C_1 \ x_1, \dots, C_n \ x_n) \ \{ \text{return } e; \}$$

in class  $C$  is represented by a non-terminal  $C\_m$ , whose body is

$$\lambda x_1. \dots \lambda x_n. \lambda this. \lambda k. \llbracket e \rrbracket_k$$

where  $k$  is the continuation parameter and  $\llbracket e \rrbracket_k$  denotes the translation of  $e$ , which passes the result of method execution to  $k$ . For example, method `add` in class `S` is represented by non-terminal  $S\_add$ , whose body is

$$\lambda n. \lambda this. \lambda k. \llbracket \text{Nat } p' = \dots; \text{return new S}(p'); \rrbracket_k$$

Then, method invocation is expressed as self-application [18]. For example, invocation of `add` on an `S` object with a `Z` object as an argument is expressed by

$$S\_add \langle Z\_add, Z\_pred, Z\_rand \rangle \langle S\_add, S\_pred, S\_rand \rangle k$$

where  $k$  is the current continuation. Note that  $S\_add$  is applied to a tuple that contains itself.

To deal with fields, each method is further abstracted by values of fields of `this`. So, the body of  $S\_add$  is in fact

$$\lambda p. \lambda n. \lambda this. \lambda k. \llbracket \text{Nat } p' = \dots; \text{return new S}(p'); \rrbracket_k,$$

where  $p$  stands for `this.p` inside the method body. Although this scheme only supports field access of the form `this.f`, field access to any expressions other than `this` (as allowed in FJ) can be expressed by using “getter” methods. A non-terminal representing a method will be applied to initial field values when an object is instantiated. For example, object instantiation `new S(p')` is represented by  $\langle S\_add p', S\_pred p', S\_rand p' \rangle$ . By using pattern-matching for  $\lambda$ , method `add` in class `S` is expressed by the following two rules:

$$\begin{aligned} S\_add &\mapsto \lambda \langle p_a, p_p, p_r \rangle. \lambda \langle n_a, n_p, n_r \rangle. \lambda \langle this_a, this_p, this_r \rangle. \lambda k. \\ &\quad p_a \langle n_a, n_p, n_r \rangle \langle p_a, p_p, p_r \rangle (F k), \\ F &\mapsto \lambda k'. \lambda \langle p'_a, p'_p, p'_r \rangle. \\ &\quad k' \langle S\_add \langle p'_a, p'_p, p'_r \rangle, S\_pred \langle p'_a, p'_p, p'_r \rangle, S\_rand \langle p'_a, p'_p, p'_r \rangle \rangle \end{aligned}$$

where  $F$  stands for the continuation of the local variable definition `Nat p' = ...;`.

A global action  $a$  is represented by a tree node  $a$ ; non-deterministic choice is by the node `br` of arity 2. The (translation of the) main expression is given as the initial continuation a constant function that returns the tree node `e` of arity 0. So, in order to verify that the program does not `fail`, it suffices to verify that the generated tree consists only of nodes `br` and `e`.

There are some further twists in giving encoding:

- We address the problem of the lack of subtyping in  $\mu$ HORS as follows. We represent every object as a tuple of the *same length*  $\ell$ , where  $\ell$  is the number of the methods defined in the whole program. If a certain method is undefined, we just insert a dummy function  $\lambda \tilde{x}. \lambda k. \text{fail}$  in the corresponding position of the tuple. The dummy function just outputs `fail` to signal `NoSuchMethodError` whenever it is called. For example, for our running example, if there is another class that defines a method `print`, a `Z` object would be expressed as a tuple  $\langle Z\_add, Z\_pred, Z\_rand, Z\_print \rangle$  where  $Z\_print = \lambda this. \lambda k. \text{fail}$ .

– As careful readers may recall,  $\mu$ HORS does not have tuples as primitives. Thus, we actually curry all the functions that take tuples as arguments. For example, the example of method invocation shown above is replaced with

$S\_add\ Z\_add\ Z\_pred\ Z\_rand\ S\_add\ S\_pred\ S\_rand\ k$

Thanks to the CPS representation, tuples occur only in the argument positions, so that currying transformation eliminates all the tuples. Thus, we can assume below, without loss of generality, that  $\mu$ HORS has been extended with tuples and tuple types.

It is easy to see that the resulting encoding of an object is well-typed. Let  $\{m_1, \dots, m_\ell\}$  be all the method names in the program, and  $\{n_1, \dots, n_\ell\}$  be their arities. Then, the encoding of every object would have the same recursive sort  $\kappa_o$ , given by:

$$\kappa_o = \kappa_{m_1} \times \dots \times \kappa_{m_\ell} \quad \kappa_{m_i} = \underbrace{\kappa_o \rightarrow \dots \rightarrow \kappa_o}_{n_i} \rightarrow \underbrace{(\kappa_o \rightarrow o)}_{\text{type of continuation}} \rightarrow o$$

Now, we develop translation formally. Let  $\ell$  be the number of defined method names and  $Meth = \{m_1, \dots, m_\ell\}$  be the set of defined method names, i.e.,  $\{m \mid \exists C.mbody(m, C) = \tilde{x}.s\}$ , in a given class table. We write  $arity(m)$  for the number of arguments to method  $m$  and  $\langle \mathbf{x} \rangle$  for  $\langle x_1, \dots, x_\ell \rangle$ .

$$\begin{aligned} \llbracket x \rrbracket &= \langle \mathbf{x} \rangle \\ \llbracket \text{this}.f \rrbracket &= \langle \mathbf{f} \rangle \\ \llbracket \text{new } C(\tilde{v}) \rrbracket &= \langle C.m_1 \llbracket v_1 \rrbracket \dots \llbracket v_n \rrbracket, \dots, C.m_\ell \llbracket v_1 \rrbracket \dots \llbracket v_n \rrbracket \rangle \\ \llbracket \text{return } v \rrbracket_k &= (k \llbracket v \rrbracket, \emptyset) \\ \llbracket C\ x = v_0.m_i(\tilde{v}); s \rrbracket_k &= (t_i \llbracket v_1 \rrbracket \dots \llbracket v_n \rrbracket \llbracket v_0 \rrbracket\ (F \langle \mathbf{z}_1 \rangle \dots \langle \mathbf{z}_m \rangle k), \mathcal{R} \cup \{F \mapsto \lambda \langle \mathbf{z}_1 \rangle \dots \lambda \langle \mathbf{z}_m \rangle . \lambda k'. \lambda \langle \mathbf{x} \rangle . t\}) \\ &\quad \text{where } \langle t_1, \dots, t_\ell \rangle = \llbracket v_0 \rrbracket, \\ &\quad (t, \mathcal{R}) = \llbracket s \rrbracket_{k'}, \{z_1, \dots, z_m\} = FV(s) \setminus \{x\} \text{ for fresh } F \text{ and } k', \\ \llbracket a; s \rrbracket_k &= (a\ (t\ k), \mathcal{R}) \quad \text{where } (t, \mathcal{R}) = \llbracket s \rrbracket_k \\ \llbracket s_1 \square s_2 \rrbracket_k &= (\text{br } (t_1\ k)\ (t_2\ k), \mathcal{R}_1 \cup \mathcal{R}_2) \quad \text{where } (t_i, \mathcal{R}_i) = \llbracket s_i \rrbracket_k \text{ for } i = 1, 2 \\ \llbracket \text{fail} \rrbracket_k &= (\text{fail}, \emptyset) \end{aligned}$$

**Fig. 5.** Translation of values and statements.

*Translation of Values and Statements.* We first show how values and statements are translated in Figure 5. A value is translated to a tuple of methods. A variable or field access is expressed by a tuple of variables of length  $\ell$ . An object instantiation  $\text{new } C(\tilde{v})$  is expressed by a tuple of applications of methods to field values. As we have mentioned,  $C.m_i$  is a non-terminal that expresses the body of method  $m_i$  in class  $C$ . For example,

$$\begin{aligned} \llbracket \text{new } S(\mathbf{p}') \rrbracket &= \\ \langle S\_add \langle p'_a, p'_p, p'_r \rangle, S\_pred \langle p'_a, p'_p, p'_r \rangle, S\_rand \langle p'_a, p'_p, p'_r \rangle \rangle . \end{aligned}$$

(We use subscripts  $a, p$  and  $r$  to mean methods **add**, **pred** and **rand**, rather than numbers.)

A statement is translated to a pair  $(t, \mathcal{R})$  of a  $\lambda$ -term and a set of rewriting rules. Translation is essentially call-by-value CPS-translation and the definitions of continuation functions are returned as the set of rewriting rules. A statement **return**  $v$ ; is translated to an application of the given continuation to the value  $v$ ; no continuation function is generated. In translation of a method invocation,  $t_i$ , the  $i$ -th element of  $v_0$ , represents the method to be invoked. It is applied to the formal arguments, the receiver object, and then the continuation  $F$  of the invocation. The body of  $F$  is obtained by translating  $s$ ; since  $s$  may refer to other variables than  $x$  (the result of method invocation),  $F$  takes values of those free variables as arguments. So, the translation combines CPS translation and  $\lambda$ -lifting [16]. Translation of action signaling, non-deterministic choice, and failure is straightforward.

For example, given  $k$ , the body of **add** in class **S** is translated to

$$\begin{aligned} & (p_a \langle n_a, n_p, n_r \rangle \langle p_a, p_p, p_r \rangle (F k), \\ & \{F \mapsto \lambda k'. \lambda \langle p'_a, p'_p, p'_r \rangle. \\ & \quad k' \langle S\_add \langle p'_a, p'_p, p'_r \rangle, \\ & \quad \quad S\_pred \langle p'_a, p'_p, p'_r \rangle, S\_rand \langle p'_a, p'_p, p'_r \rangle \rangle\}). \end{aligned}$$

The invocation of **add** on  $\mathbf{p}$  is expressed by application of  $p_a$  to terms that express the actual argument  $\mathbf{n}$ , the receiver, and continuation  $F$ .

$$\begin{aligned} \llbracket \text{class } C \text{ extends } D \{C_1 f_1; \dots C_n f_n; K \widetilde{M}\} \rrbracket = & \\ (\bigcup \{ \{C\_m_i \mapsto \lambda \langle \mathbf{g}_1 \rangle. \dots \lambda \langle \mathbf{g}_k \rangle. t\} \cup \mathcal{R} \mid & \\ \text{fields}(C) = D_1 g_1, \dots, D_k g_k, C_0 m_i (\widetilde{C} \widetilde{x}) \{s\} \in \widetilde{M}, (t, \mathcal{R}) = \llbracket \widetilde{x}.s \rrbracket \} & \\ \cup \{C\_m_i \mapsto \lambda \langle \mathbf{f}_1 \rangle. \dots \lambda \langle \mathbf{f}_n \rangle. \lambda \langle \mathbf{g}_1 \rangle. \dots \lambda \langle \mathbf{g}_k \rangle. \lambda k. D\_m_i \langle \mathbf{g}_1 \rangle \dots \langle \mathbf{g}_k \rangle k \mid & \\ m_i \notin \widetilde{M}, \text{fields}(D) = D_1 g_1, \dots, D_k g_k \} & \\ \llbracket \widetilde{x}.s \rrbracket = (\lambda \langle \mathbf{x}_1 \rangle. \dots \lambda \langle \mathbf{x}_n \rangle. \lambda \langle \mathbf{this} \rangle. \lambda k. t, \mathcal{R}) \quad \text{where } (t, \mathcal{R}) = \llbracket s \rrbracket_k \text{ for fresh } k & \end{aligned}$$

$$\begin{aligned} \llbracket (CT, s) \rrbracket = (\mathcal{N}, \Sigma, \mathcal{R}, S) & \\ \text{where } \Sigma = \{\mathbf{br} \mapsto 2, \mathbf{fail} \mapsto 0, \mathbf{e} \mapsto 0\} \cup \{a \mapsto 1 \mid a \in \text{Actions}\} & \\ \mathcal{R} = \{\text{Object\_}m_i \mapsto \lambda \langle \mathbf{x}_1 \rangle. \dots \lambda \langle \mathbf{x}_{n+1} \rangle. \lambda k. \mathbf{fail} \mid m_i \in \text{Meth}, \text{arity}(m_i) = n\} & \\ \cup \bigcup_C \llbracket CT(C) \rrbracket \cup \{S \mapsto t, \text{End} \mapsto \lambda \langle \mathbf{x} \rangle. \mathbf{e}\} \cup \mathcal{R}' & \\ \text{where } (t, \mathcal{R}') = \llbracket s \rrbracket_{\text{End}} & \\ \mathcal{N} = \{F \mapsto \text{OType}(n) \mid \mathcal{R}(F) = \lambda \langle \mathbf{x}_1 \rangle. \dots \lambda \langle \mathbf{x}_n \rangle. \lambda k. t\} \cup \{S \mapsto \mathbf{o}, \text{End} \mapsto \kappa_o \rightarrow \mathbf{o}\} & \\ \text{where } \begin{cases} \kappa_o & = \kappa_1 \times \dots \times \kappa_\ell \\ \text{OType}(0) & = (\kappa_o \rightarrow \mathbf{o}) \rightarrow \mathbf{o} \\ \text{OType}(n+1) & = \kappa_o \rightarrow \text{OType}(n) \end{cases} & \end{aligned}$$

**Fig. 6.** Translation of Classes and Programs.

*Translation of Class Definitions and Programs.* A class definition is translated to a set of rules in (the upper half of) Figure 6. For each class  $C$ , non-terminals  $C.m_1, \dots, C.m_\ell$  are generated. When  $m_i$  is defined in  $C$ , its body is translated to  $t$  (with a set  $\mathcal{R}$  of continuation functions) and  $t$  is further parametrized by fields. Otherwise,  $C.m_i$  calls the inherited definition  $D.m_i$ . Since subclass  $C$  may have additional fields,  $C.m_i$  has to be further parametrized by those fields. As we will see below,  $Object.m_i$  is a function that just signals a failure. So, such functions will be inherited to subclasses until a subclass gives a definition of  $m_i$ .

For example, `[[class Z ...]]` includes

$$Z.add \mapsto \lambda k. Nat.add k$$

since `add` is inherited from the superclass and `[[class S ...]]` includes the following two rules:

$$\begin{aligned} S.add &\mapsto \lambda \langle p_a, p_p, p_r \rangle. \lambda \langle n_a, n_p, n_r \rangle. \lambda \langle this_a, this_p, this_r \rangle. \\ &\quad \lambda k. p_a \langle n_a, n_p, n_r \rangle \langle p_a, p_p, p_r \rangle (F k), \\ F &\mapsto \lambda k'. \lambda \langle p'_a, p'_p, p'_r \rangle. \\ &\quad k' \langle S.add \langle p'_a, p'_p, p'_r \rangle, \\ &\quad S.pred \langle p'_a, p'_p, p'_r \rangle, S.rand \langle p'_a, p'_p, p'_r \rangle \rangle \end{aligned}$$

Here, the formal parameters  $p_a$ ,  $p_p$ , and  $p_r$  stand for the value of field `p` and  $n_a$ ,  $n_p$ , and  $n_r$  for the parameter to method `add`.

Finally, we give translation of a whole FJ-- program to a recursion scheme as in (the lower half of) Figure 6. Terminals include special symbols `br`, `fail` and `e` to denote branching caused by non-deterministic choice, failure and termination of program execution, respectively. Actions are terminals of arity 1. Rules are obtained by collecting all class translations (including *Object*, which provides only failing methods) and translation of the main statement. Non-terminal *End* stands for an initial continuation, which ends the execution immediately. The sorts of non-terminals are easily computed by function *OType*, thanks to the fact that the bodies of most non-terminals are of the form

$$\lambda \langle x_1 \rangle. \dots \lambda \langle x_n \rangle. \lambda k. t$$

for some  $n$  and an applicative term  $t$ .

*Properties of the Translation.* We state properties of the translation. First, it is easy to see that the translation preserves typing, that is, a well typed FJ-- program translates to a well-formed recursion scheme.

**Theorem 7.** *If  $\vdash (CT, s) : C$ , then  $[[CT, s]]$  is a well-formed  $\mu HORS$ .*

Note that even when a given FJ-- program is not well typed, its translation may be a well-formed recursion scheme.

The resulting  $\mu HORS$  generates a tree that expresses as a path a sequence  $a_1, a_2, \dots$  of actions that can occur during the FJ-- program execution. More formally, the translation preserves the semantics of the program in the following sense:

**Theorem 8.** *Under a class table  $CT$ ,  $s \xrightarrow{a_1 \cdots a_n} r$  if and only if the tree generated by  $\llbracket (CT, s) \rrbracket$  contains a path  $a_1 \cdots a_n$  from the root (ignoring **br**).*

It is easy to see as a corollary that (1) a program fails with *fail*; or `NoSuchMethodError` if and only if the tree generated by the translated  $\mu$ HORS contains a path that ends with `fail` and (2) a program terminates successfully if and only if the tree generated by the translated  $\mu$ HORS contains a path that ends with `e`.

We briefly discuss how properties listed at the beginning of the section can be verified. To verify reachability, we insert a special action to program points of interest and verify whether the generated tree contains the special action. We can verify a method invocation order by inserting distinct actions to the head of each method body. Although this method would be used to verify only a *global* method invocation history, it would be possible to modify the translation to verify an object-wise method invocation history, similarly to resource usage analysis [14] via HO model checking—see Kobayashi [22] for details.

**Downcasts** Given a whole FJ-- program with casts, we can transform it into an FJ-- program without casts, by adding a method *castToC* to class *C*, for every occurrence of  $(C)e$ . The definition of *castToC* for a class *C* is:

$$C \text{ castToC}() \{ \text{return this}; \}$$

Note that *castToC* is defined only in *C* and its subclasses. So, downcasts will be transformed into an ill typed method invocation, although the recursion scheme after translation will be well formed thanks to the generation of failing methods. (It is easy to modify the translation so that failures due to `NoSuchMethodError` and `ClassCastException` are distinguished.)

## 4.2 Model-Checking Higher-Order Multi-Threaded Programs

This section discusses how to apply the extended HO model checking to verification of multi-threaded programs, where each thread may use higher-order functions and recursion. For the sake of simplicity, we discuss only programs consisting of two threads, whose syntax is given by:

$$\begin{aligned} P \text{ (programs)} &::= M_1 \parallel M_2 \\ M \text{ (threads)} &::= () \mid a \mid x \mid \text{fun}(f, x, M) \mid M_1 M_2 \mid M_1 \square M_2 \end{aligned}$$

A program  $P = M_1 \parallel M_2$  executes two threads  $M_1$  and  $M_2$  concurrently, where  $M_1$  and  $M_2$  are (call-by-value) higher-order functional programs with side effects. The expression  $a$  performs a global action  $a$ , and evaluates to the unit value  $()$ . We keep global actions abstract, so that various synchronization primitives and shared memory can be modeled (see examples given below). The expression  $\text{fun}(f, x, M)$  describes a recursive function  $f$  such that  $f(x) = M$ . When  $f$  does not occur in  $M$ , we write  $\lambda x.M$  for  $\text{fun}(f, x, M)$ . We also write **let**  $x = M_1$  **in**  $M_2$  for  $(\lambda x.M_2)M_1$ , and further abbreviate it to  $M_1; M_2$  when  $x$  does not occur in

$M_2$ .  $M_1 \square M_2$  evaluates  $M_1$  or  $M_2$  non-deterministically. The formal semantics of the language is given in Appendix B. The goal of verification is, given a program  $M$  and a property  $\psi$  on global action sequences, to check whether all the possible action sequences of  $M$  satisfy  $\psi$ .

*Example 12.* We can model programs accessing finite shared memory. For a one-bit shared variable  $x$ , prepare actions  $\text{read}_{x,i}$  and  $\text{write}_{x,i}$  for  $i \in \{0, 1\}$ . Then, **let**  $y = !x$  **in**  $M$  and  $x := i$ ;  $M$  can be expressed by  $(\text{read}_{x,0}; [0/y]M) \square (\text{read}_{x,1}; [1/y]M)$  and  $\text{write}_{x,i}; M$  respectively. Then, to check whether every (valid) execution of  $P$  satisfies a property  $\psi$ , it suffices to check that  $\psi$  is true of any path that represents valid read/write sequences, i.e. the most recent write action before each read action  $\text{read}_{x,i}$  must be  $\text{write}_{x,i}$ , not  $\text{write}_{x,1-i}$ .

*Example 13.* Let  $M$  be the following thread:

**let**  $\text{sync } f = \text{lock}; f(); \text{unlock}; \text{sync } f$  **in** **let**  $cr\ x = \text{enter}; \text{exit}$  **in**  $\text{sync } cr$ ,

which models a thread acquiring a global lock before entering a critical section. We may then wish to verify that the global actions **enter** and **exit** can occur only alternately, as long as **lock** and **unlock** occur alternately.  $\square$

We can reduce verification problems for multi-threads to extended HO model checking problems by transforming a given program to a  $\mu\text{HORS}$  that generates a tree describing all the possible global action sequences. The ideas of the transformation are: (i) transform each thread to CPS (continuation-passing style) to correctly model the order of actions, as in [22], and (ii) apply each thread to a scheduler, and let a thread pass the control to the scheduler non-deterministically after each global action. The translation from programs to  $\mu\text{HORS}$  is:<sup>5</sup>

$$\begin{aligned} (M_1 \parallel M_2)^\dagger &= \mathbf{br} (Sched (M_1^\dagger \lambda x. \mathbf{e}) (M_2^\dagger \lambda x. \mathbf{e})) (Sched (M_2^\dagger \lambda x. \mathbf{e}) (M_1^\dagger \lambda x. \mathbf{e})) \\ ()^\dagger &= \lambda k. \lambda g. k \mathbf{e} g \quad x^\dagger = \lambda k. \lambda g. k x g \quad \mathbf{fun}(f, x, M)^\dagger = \lambda k. \lambda g. k \mathbf{fun}(f, x, M^\dagger) g \\ (M_1 M_2)^\dagger &= \lambda k. \lambda g. M_1^\dagger (\lambda f. M_2^\dagger \lambda x. f x k) g \\ (M_1 \square M_2)^\dagger &= \lambda k. \lambda g. \mathbf{br} (M_1^\dagger k g) (M_2^\dagger k g) \quad a^\dagger = \lambda k. \lambda g. a (\mathbf{br} (k \mathbf{e} g) (g (k \mathbf{e}))) \end{aligned}$$

Here, the non-terminal *Sched* is defined by the rule  $Sched\ x\ y \rightarrow x\ (Sched\ y)$ , which schedules  $x$  first, passing to it the global continuation  $Sched\ y$  (which will schedule  $y$  next). The terminal symbol **br** represents a non-deterministic branch. On the righthand side of the last translation rule,  $a$  and  $\mathbf{e}$  are terminal symbols of arity 1 and 0 respectively. The program  $M_1 \parallel M_2$  is translated to a tree-generating program, which either schedules  $M_1$  then  $M_2$ , or  $M_2$  then  $M_1$ . Apart from the global action (the last rule), the translation of a thread is essentially the standard call-by-value CPS transformation except that a global continuation is passed as an additional parameter (in fact, with  $\eta$ -conversion, the transformation is exactly the same as the ordinary CPS transformation). The global action  $a$  is

<sup>5</sup> Here, for the sake of simplicity, we represent  $\mu\text{HORS}$  as an ordinary functional program with tree constructors. By applying  $\lambda$ -lifting, we get a system of top-level function definitions that conforms to the syntax of  $\mu\text{HORS}$  in Section 2.2.

transformed to a tree node  $a$ , followed by a non-deterministic branch (expressed by **br**). The first branch evaluates the local continuation, while the second branch yields the control to the other thread by invoking the global continuation  $g$ .

The transformation above preserves typing, in the sense that if  $P$  is a simply-typed program (where the global action  $a$  and non-determinism  $\square$  are given types  $()$  and  $\forall\alpha.(\alpha \rightarrow \alpha \rightarrow \alpha)$  respectively), then  $P^\dagger$  is a (well-typed)  $\mu$ HORS. To show this, let us define the translation of (simple) types by:

$$\begin{aligned} \mathbf{unit}^\dagger &= \circ \\ (\tau_1 \rightarrow \tau_2)^\dagger &= \tau_1^\dagger \rightarrow (\tau_2^\dagger \rightarrow \tau_{\text{Ans}}) \rightarrow \tau_{\text{Ans}}, \end{aligned}$$

where  $\tau_{\text{Ans}}$  is the recursive type defined by:

$$\begin{aligned} \tau_{\text{Ans}} &= \tau_{\text{Gcont}} \rightarrow \circ \\ \tau_{\text{Gcont}} &= \tau_{\text{Ans}} \rightarrow \circ \end{aligned}$$

We extend the encoding to type environments by:

$$(x_1 : \tau_1, \dots, x_n : \tau_n)^\dagger = x_1 : \tau_1^\dagger, \dots, x_n : \tau_n^\dagger.$$

Then, we have:

**Lemma 2.** *Let  $M$  be a thread. If  $\Gamma \vdash_{\text{ST}} M : \tau$ , then  $\Gamma^\dagger \vdash M : (\tau^\dagger \rightarrow \tau_{\text{Ans}}) \rightarrow \tau_{\text{Ans}}$  holds.*

*Proof.* The proof follows by induction on the structure of  $M$ . If  $M = a$ , then we have  $\Gamma^\dagger \vdash a : \circ \rightarrow \circ$  (recall that  $a$  is a unary terminal symbol after the translation). We also have  $k : \mathbf{unit}^\dagger \rightarrow \tau_{\text{Ans}}, g : \tau_{\text{Gcont}} \vdash k \mathbf{e} g$  and  $k : \mathbf{unit}^\dagger \rightarrow \tau_{\text{Ans}}, g : \tau_{\text{Gcont}} \vdash g(k \mathbf{e})$ . Thus, we have  $\Gamma^\dagger \vdash a^\dagger : (\mathbf{unit}^\dagger \rightarrow \tau_{\text{Ans}}) \rightarrow \tau_{\text{Ans}}$  as required. The other cases are straightforward; indeed, it is a standard result about the call-by-value CPS transformation [31].

Please note that in the above lemma, it is essential that we have recursive types; otherwise  $a^\dagger$  cannot be typed. By assigning the type  $\tau_{\text{Ans}} \rightarrow \tau_{\text{Ans}} \rightarrow \circ$  to *Sched*, we have:

**Corollary 9** *Let  $P$  be a program (of the form  $M_1 \parallel M_2$ ). If  $\emptyset \vdash_{\text{ST}} P : ()$ , then  $\emptyset \vdash P^\dagger : \circ$  holds.*

By the definition of the transformation above, it should be clear that  $P$  has a sequence of global actions  $a_1 a_2 \cdots a_n$  if and only if the tree generated by  $P^\dagger$  has a path labeled with  $a_1 a_2 \cdots a_n$  (with **br** ignored). Thus, verification of multi-threaded programs has been reduced to  $\mu$ HORS model checking. *Sched* is given type  $\tau_{\text{Thread}} \rightarrow \tau_{\text{Thread}} \rightarrow \circ$ , where  $\tau_{\text{Thread}} = \tau_{\text{Gcont}} \rightarrow \circ$  and  $\tau_{\text{Gcont}} = \tau_{\text{Thread}} \rightarrow \circ$ , where  $\tau_{\text{Thread}}$  and  $\tau_{\text{Gcont}}$  are the types of threads (which take a global continuation as an argument) global continuations respectively.

*Example 14.* Recall Example 13. The following is a hand-optimized version of  $\mu$ HORS obtained by translating the parallel composition of two copies of  $M$ .

```

S -> Sched M M.      Sched x y -> x (Sched y).
M g -> Sync Cr E g.  E g -> end.
Sync f k g -> lock (br (C1 f k g) (g (C1 f k))).
C1 f k g -> f (C2 f k) g.
C2 f k g -> unlock(br (Sync f k g) (g (Sync f k))).
Cr k g -> enter (br (C3 k g) (g (C3 k))).
C3 k g -> exit (br (k g) (g k)).

```

As reported in Section 5, our model checker can verify that **enter** and **exit** can occur only alternately, as long as **lock** and **unlock** occur alternately, which implies that the two threads in the source program never enter critical sections simultaneously.  $\square$

*Context-bounded model checking* Qadeer and Rehof [37] showed that model checking of concurrent pushdown systems (or multi-threaded programs with *first-order* recursion) is decidable *if the number of context switches is bounded by a constant*. Our translation given above yields a generalization of the result: context-bounded model checking of multi-threaded, *higher-order* recursive programs is decidable. To obtain the result, it suffices to replace the scheduler *Sched* with *Sched<sub>ℓ</sub>* given below, which allows only  $\ell$  context switches:

$$Sched_0 x y \rightarrow e \quad Sched_{i+1} x y \rightarrow x (Sched_i y)$$

By the definition of the modified translation above, it should be trivial that  $P$  with context-bound  $\ell$  has a sequence of global actions  $a_1 a_2 \cdots a_n$  if and only if the tree generated by  $P^{\dagger \ell}$  has a path labeled with  $a_1 a_2 \cdots a_n$  (with **br** ignored). Furthermore, the modified translation guarantees a stronger type-preservation property:

**Lemma 3.** *If  $\emptyset \vdash_{\text{ST}} P : ()$ , then  $\emptyset \vdash P^{\dagger \ell} : \circ$  is derivable (with intersection types but) without using recursive types.*

Thus,  $P^{\dagger \ell}$  is an ordinary HORS (extended with finite intersection types). As an immediate corollary of the above properties and the decidability of HORS model checking [33], we obtain that context-bounded model checking of multi-threaded higher-order programs is decidable.<sup>6</sup>

To show Lemma 3, we modify the encoding of types by:

$$\begin{aligned} \mathbf{unit}^{\dagger i} &= \circ \\ (\tau_1 \rightarrow \tau_2)^{\dagger i} &= \tau_1^{\dagger i} \rightarrow (\tau_2^{\dagger i} \rightarrow \sigma_{\text{Ans}, i}) \rightarrow \sigma_{\text{Ans}, i}. \end{aligned}$$

where  $\sigma_{\text{Ans}, i}$  is defined by:

$$\sigma_{\text{Ans}, 0} = \top \quad \sigma_{\text{Ans}, i+1} = \tau_{\text{Gcont}, i} \rightarrow \circ \quad \tau_{\text{Gcont}, i} = \sigma_{\text{Ans}, i} \rightarrow \circ$$

The encoding is pointwise extended to that on type environments. The following lemma is analogous to Lemma 2.

<sup>6</sup> We have considered only programs with two threads, but this restriction can be easily relaxed by using the same technique as Qadeer and Rehof [37].

**Lemma 4.** *Let  $M$  be a thread and  $\ell > 0$ . If  $\Gamma \vdash_{\text{ST}} M : \tau$ , then  $\Gamma^{\dagger\ell} \vdash M^{\dagger} : (\tau^{\dagger\ell} \rightarrow \sigma_{\text{Ans},\ell}) \rightarrow \sigma_{\text{Ans},\ell}$  holds.*

*Proof.* The proof follows by induction on the structure of  $M$ . The only non-trivial case is when  $M = a$ . Let  $\Gamma_1 = k : \mathbf{unit}^{\dagger} \rightarrow \sigma_{\text{Ans},\ell}, g : \tau_{\text{Gcont},\ell-1}$ . Then, we obtain  $\Gamma^{\dagger\ell}, \Gamma_1 \vdash k \mathbf{e} g : \circ$  immediately. As  $\sigma_{\text{Ans},1} \leq \sigma_{\text{Ans},0} = \top$ , we have  $\sigma_{\text{Ans},\ell} \leq \sigma_{\text{Ans},\ell-1}$ . Thus, we also obtain  $\Gamma^{\dagger\ell}, \Gamma_1 \vdash g(k \mathbf{e}) : \circ$ . Therefore, we have  $\Gamma^{\dagger\ell} \vdash a^{\dagger} : (\mathbf{unit}^{\dagger} \rightarrow \sigma_{\text{Ans},\ell}) \rightarrow \sigma_{\text{Ans},\ell}$  as required.

Lemma 3 follows as a corollary of the above lemma and the fact that  $Sched_i$ 's have the following (non-recursive) types:

$$\begin{aligned} Sched_{2m} &: \sigma_{\text{Ans},m} \rightarrow \sigma_{\text{Ans},m} \rightarrow \circ \\ Sched_{2m+1} &: \sigma_{\text{Ans},m+1} \rightarrow \sigma_{\text{Ans},m} \rightarrow \circ \end{aligned}$$

## 5 Implementation and Experiments

We have implemented a prototype model checker RTRECS for  $\mu\text{HORS}$  based on the procedure FINDCERT described in Section 3. As the underlying SAT solver, we have used MiniSat 2.2 (<http://minisat.se/MiniSat.html>). We have reused some code of the model checker TRECS (for HORS without recursive types), but the core algorithm has been written from scratch (as the new algorithm is radically different from that of TRECS). We have also implemented a translator from FJ programs to  $\mu\text{HORS}$  based on Section 4.1.

The implementation is based on the procedure FINDCERT in Figure 4, except for the following points. RTRECS first performs an equality-based flow analysis [35] before model checking, and uses it as the equivalence relation  $\sim$ .  $\mathcal{U}$  is also over-approximated by using the result of the flow analysis; thus, in the current implementation, the reductions of terms (3rd line in Figure 4) are performed only for finding a counter-example, without using the rule R-EQ.

Table 1 summarizes the result of preliminary experiments. (For space restriction, we omit some results, which are found in [25].) The programs used for the experiments and the web interface for our prototype are available at <http://www.kb.is.s.u-tokyo.ac.jp/~koba/fjmc/>. The columns “#lines” and “#rules” show the number of lines of the source FJ program (if applicable) and the number of the rules of  $\mu\text{HORS}$ . The column “#states” shows the number of states of the property automaton. The column “ $k$ ” shows  $k$  of  $\Gamma' \sqsubseteq_s \Gamma^{(k)}$  in Section 3.2. The column “answer” shows whether the property was judged to be satisfied (Y) or not (N). (The actual implementation returns recursive intersection types as a certificate in the former case, and a counter-example in the latter case.) The column “#sat” shows the number of sat clauses (i.e., the number of disjunctive formulas in conjunctive normal form) for the final call of the SAT solver. (For `ski1`, a counterexample was found before the SAT solver is called.) The column “time” shows the running time (excluding the time for translation from FJ to  $\mu\text{HORS}$ , which is anyway quite small).

$\mathcal{G}_1$  and  $\mathcal{G}_2$  are from Examples 2 and 3, where the checked property is expressed by  $\mathcal{B}_1$  in Example 4. **Thread** is the  $\mu$ HORS given in Example 14, and **Thread2** is its variation, obtained by replacing the definition of  $cr$  in Example 13 by  $cr\ x = ()\square(cr\ x; \mathbf{enter}; \mathbf{exit})$ . The other programs were obtained from FJ programs, based on the translation discussed in Section 4.1, and except for **Twofiles**, we verified that the programs do not fail (where the meaning of “failure” depends on each program, as explained below). **Pred** is the running example in Section 4. **Ski1** and **Ski2** are implementations of SKI combinators in FJ [41], which were further translated to recursion schemes. **Ski1** and **Ski2** reduce  $SII$  and  $SII(SII)$  respectively, and fails if the reduction terminates. As the reduction of  $SII$  terminates but  $SII(SII)$  does not, the answers for **Ski1** and **Ski2** are N and Y respectively. The next six are list-manipulating programs (implemented as objects), which are small but non-trivial programs. (In fact, **L-filter** and **L-risers** are object-oriented versions of benchmark programs of the PMRS verification tool [34].) The program **L-append** repeatedly applies an **append** function (and never terminates), and fails if a method **hd** or **tl** is called for a null list object:

```
List loop_app(List l)
  { return this.loop_app(l.append(l)); }
```

The program **L-map** repeatedly applies a successor function to each element of a list (and never terminates). List constructors are generically defined so that **Object** is casted to **Nat** before each call of the successor function. The program fails if the successor function is applied to a non-number.

The program **L-app-map** repeatedly applies **map** and **append** functions:

```
class Main extends Object{ ...
  List loop_app_map(List l) {
    return this.loop_app_map
      (l.append(l).map(new Fun()));  }
  main() {
    return new Main().loop_app_map
      (new Cons(new S(new Z()),new Nil()));}
}
```

Here, **Fun** is an object representing a function that takes a natural number  $n$  and returns  $n + n$ , where natural numbers are also expressed as objects.

The program **L-even** creates a list of an even length in a non-deterministic manner<sup>7</sup>, and fails if its length is odd.

The program **L-filter** creates a list of natural numbers in a non-deterministic manner, filters out 0, and checks that the resulting list consists only of non-zero elements (and fails if it does not hold). **L-filter** creates a list of natural numbers in a non-deterministic manner, filters out 0, and checks that the resulting list consists only of non-zero elements (and fails if it does not hold). **L-risers** creates a list of natural numbers in a non-deterministic manner, splits it into a

<sup>7</sup> We have extended FJ with non-deterministic branches.

programs	#lines	#rules	answer	#sat	time
$\mathcal{G}_1$	–	2	Y	27	0.001
$\mathcal{G}_2$	–	3	Y	49	0.002
<b>Thread</b>	–	9	Y	38,171	0.580
<b>Pred</b>	21	15	Y	157	0.005
<b>Ski1</b>	40	22	N	—	0.002
<b>Ski2</b>	40	25	Y	141	0.002
<b>L-append</b>	20	30	Y	165	0.006
<b>L-map</b>	43	182	Y	738	0.235
<b>L-app-map</b>	43	212	Y	1,546	0.391
<b>L-even</b>	25	87	Y	249	0.025
<b>L-filter</b>	59	122	Y	5,964	0.491
<b>L-risers</b>	73	64	Y	17,419	0.445
<b>Twofiles</b>	28	21	Y	739,867	13.86

**Table 1.** Experimental Results (CPU: Intel(R) Xeon(R) 3GHz, Memory: 8GB). Times are in seconds.

list of lists consisting of non-decreasing sequences, and fails if a pattern match error occurs during the list processing.

**Twofiles** was prepared as an example of verification of temporal properties. It is an object-oriented version of the program that accesses two files: one for read-only, and the other for write-only [22]. We verify that the read-only (write-only, resp.) file is closed after some reads (writes, resp.).

Our model checker RTRECS could successfully verify all the programs. The verification time and the size of SAT formulas were significantly larger for **Twofiles** compared with other programs. The explosion of the size of SAT formulas for **Twofiles** is due to the size of the automaton for describing the temporal property, which blows up the number of candidates of types to be considered. More optimizations are necessary for avoiding this problem. The number  $k$  was surprisingly small for all the benchmark programs; this indicates that our choice of  $\sim$  based on the equality-based flow analysis provided a good approximation of types. Overall, the experimental results above are encouraging; we are not aware of other fully-automated (i.e. requiring no annotations), sound (i.e. no false negatives) verification tools that can verify all the programs above.

To see the effectiveness of the SAT-based approach to HO model checking, we have also compared RTRECS with two other model checkers for HORS: TRECS [21, 20] and GTRECS2 [19, 23, 26], which use radically different algorithms and cannot deal with  $\mu$ HORS. The results are summarized in Table 2.

The first two HORS **Twofiles-f**, **Fileocamlc** and **Order5** have been taken from [20], obtained by encoding resource usage verification problems [22]. **Mc91** and **Repeat** have been taken from [29], obtained by encoding reachability problems for functional programs using predicate abstraction. The other programs **Xhtmlf-id**, **Exp4-5**, and **Fibstring** have been taken from [30], [23], and [26], respectively. RTRECS is generally slower than TRECS for the problems obtained

from program verification problems. RTRECS, however, outperforms TRECS for `Exp4-5` and `Fibstring`, which generate huge trees, and are hard cases for the TRECS algorithm. GTRECS2 has been tuned to deal with such cases, but RTRECS is actually faster for `Exp4-5`. Overall, the three model checkers are complementary to each other, which is encouraging given that the implementation of RTRECS is premature compared with the other two.

HORS	TRECS	GTRECS2	RTRECS
<code>Twofiles-f</code>	0.005	0.168	13.092
<code>Fileocamlc</code>	0.01	0.109	1.028
<code>Order5</code>	0.005	–	0.719
<code>Mc91</code>	0.07	0.379	–
<code>Repeat</code>	0.012	0.211	0.024
<code>Xhtmlf-id</code>	0.663	53.675	–
<code>Exp4-5</code>	–	2.422	0.367
<code>Fibstring</code>	–	0.210	0.284

**Table 2.** Comparison of Verification Times (in seconds). “–” indicates a time-out.

## 6 Discussion

We discuss related work and limitations of our approach.

### 6.1 Related Work

The model checking of HORS has recently emerged as a new technique for verification of higher-order programs [33, 22, 30, 34, 29]. Except Tsukada and Kobayashi’s work [46], however, all the previous studies dealt with *simply-typed* recursion schemes, which are not suitable for modeling objects. Tsukada and Kobayashi [46] studied model checking of *untyped* HORS and reduced it to a type checking problem for an infinite intersection type system. The latter problem is however undecidable and they did not provide any sound (but necessarily incomplete) realistic procedure for model checking.

Several methods for model-checking functional programs have been proposed recently [40, 22, 30, 34, 44, 47], proposed recently [40, 22, 30, 34, 47], and some of them [40, 30, 34] support recursive data structures (like lists). However, it is not clear how to extend them to support general recursive types (including negative occurrences of recursive type variables). Furthermore, many of them require annotations [40, 47] and are less precise.

There are previous studies on model checking of object-oriented programs [9, 12]. To our knowledge, however, they are based on finite state model checking; Java programs are either (i) abstracted to finite state models and then finite

state model checkers are used to verify the abstract models, or (ii) directly model checked, but with an incomplete state exploration (e.g., by identifying states having the same hash values [12]; see also <http://babelfish.arc.nasa.gov/trac/jpf/wiki/intro/classification>, section “State Matching”). In the former case, because of the huge semantic gap between object-oriented programs and finite state systems, a lot of information is lost by the translation from Java programs to models. In the latter case, a “model checker” is used mainly as a bug detection tool, instead of a verification tool. In contrast, our method uses  $\mu$ HORS as models, which are as expressive as source programs. No information is lost by the translation from FJ to  $\mu$ HORS, and no false alarms can be generated (although the model checker may not terminate for some valid programs).

There is some similarity between the “state matching” used in Java PathFinder (see the URL above) and the use of equivalence relation  $\sim$  in our model checking algorithm. Both of them merge states that are “equal”. The fundamental difference is that Java PathFinder stops the reduction if an “equal” state is already visited, while our algorithm does not; rather, our algorithm generate even new states (e.g.,  $t_1t'_2$  and  $t'_1t_2$  if  $t_1t_2$  and  $t'_1t'_2$  are visited and  $t_1 \sim t'_1$ ) and explore reductions from them. This difference makes Java PathFinder efficient as a bug finding tool but not sound as a verification tool, and our tool slow but sound as a verification tool.

Another major approach to verification of object-oriented programs is to generate verification conditions from (annotated) programs, and then discharge them by theorem provers [2, 3]. Although there are some studies to infer invariants, this approach basically requires a lot of human intervention, such as annotations of pre/post-conditions for each method.

There are a number of studies on static analyses of object-oriented programs: see surveys [36, 8, 13, 39]. There are also studies of type systems [4, 5] and separation logic [36, 8] for verification of detailed properties about objects, which usually requires some annotations (e.g., typestate/aliasing annotations and/or pre/post-conditions of methods). Skalka [43] also proposed a method for automatically verifying temporal properties. His method is a combination of types and model checking, where a model is extracted through type inference and then it is model-checked. The first step (for extracting models through type inference) loses much information about source programs, while the first step in our method (the transformation of source programs into  $\mu$ HORS) loses no information. In general, the distinguished features of our method are: (i) it is fully automated yet very precise, (ii) it can generate a counter-example when a property is not satisfied, and (iii) it can verify temporal properties. For the last point, typestates [4, 5] can also verify some temporal properties, but they require special annotations. On the downside, our method is currently applicable only to functional objects. Rowe and Bakel [41] proposed an intersection type system for reasoning about object-oriented programs, but did not give an automated verification algorithm.

There are many studies on model checking of recursive parallel programs [37, 17, 11], which obtain decidable fragments by restricting synchronization prim-

itives or applying approximations. It is interesting to see whether each result can be extended to higher-order, recursive parallel programs (besides context-bounded model checking discussed in Section 4.2), and if so, whether the decidability can be obtained via reduction to HORS model checking as in Section 4.2, and whether tight complexity bounds can be obtained in that way. Note that the encoding in Section 4.2 does not give a good complexity bound (due to the use of CPS).

The implementation of our model checker uses the result of flow analysis to choose the equivalence relation  $\sim$  and saturate the set  $\mathcal{U}$ . Our current implementation uses an imprecise, equality-based analysis, but in principle, it can be replaced by the result of any sound flow analysis, such as  $k$ -CFA [42].

## 6.2 Limitations

The fundamental limitations of our approach to automated program verification are:

1. Incompleteness of the recursive intersection type system with respect to the  $\mu$ HORS model checking problem.

This means that there are correct programs that are not typable in the type system, in which case, our model checker never terminates. We have not yet obtained a clear characterization of such programs, but we expect that the class of such programs include those whose correctness proofs require numerical properties on multiple arguments (such as  $x + y \geq z$ ).

2. Undecidability (only semi-decidability) of the typability in the recursive intersection type system (c.f. Theorem 3, 2).

This means that even if a program is typable in the type system, there is no theoretical upper bound (even non-elementary one) on the time for model-checking to terminate.

To address the first limitation, we plan to treat numerical values as primitives and combine our method with predicate abstraction (as we did for verification of functional programs [29]). Extending the expressive power of recursive intersection types (so that we can use non-regular types) is also another interesting approach to address the first limitation. As for the second limitation, although we cannot bound the time complexity with respect to the program size, we can do so with respect to the size of the smallest certificate (i.e., the smallest witness type environment  $\Gamma$  such that  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$ ). Thus, if typical programs (and properties) have small certificates, then our approach may work well in practice (and the result of our preliminary experiments seem to indicate that this may indeed be the case). We should however refine the algorithm and implementation techniques further.

Our current verification tool for object-oriented programs only support FJ. The main obstacles to applying our approach to Java would be to support imperative field updates and concurrency; we believe that the other features (such as exceptions) can be easily handled either by encoding into FJ or by the translation to  $\mu$ HORS (c.f. encoding of control primitives of functional programs into

HORS [22, 20]). Since  $\mu$ HORS has recursive types, in theory, field updates and concurrency can also be encoded; we can use state passing for the former, and explicit representation of a scheduler for the latter, as discussed in Section 4.2. We expect, however, that more clever encoding and abstraction techniques are needed to make the whole verification work effectively in practice.

## 7 Conclusion

We have proposed a model checking procedure for  $\mu$ HORS, an extension of higher-order recursion schemes with recursive types, and shown its applications to verification of object-oriented programs and concurrent programs. The model-checking procedure is incomplete (due to the inherent undecidability of the model checking problem), but is relatively complete with respect to a type system with recursive intersection types. We have also implemented a prototype model checker and carried out preliminary experiments. Although there is a lot of work to be done to apply the proposed method to practice, we believe this is a good first step to get a fully-automated (i.e., requiring no annotations), sound (i.e., usable for verification, not just for bug finding) and precise software model checker for high-level programming languages that support objects, concurrency, and higher-order functions.

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## Appendix

### A Proofs

#### A.1 Proof of Theorem 4

Below we assume that  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  and  $\sim \subseteq \sim_{\Gamma}$ . We also assume that  $(\mathcal{X}_0, \mathcal{U}_0) \rightarrow_{\sim} (\mathcal{X}_1, \mathcal{U}_1) \rightarrow_{\sim} (\mathcal{X}_2, \mathcal{U}_2) \rightarrow_{\sim} \dots$  is a fair reduction sequence where  $\mathcal{X}_0 = \{S\}$  and  $\mathcal{U}_0 = \{(S, q_{\mathcal{B},0})\}$ , and that  $\mathcal{X}$  and  $\mathcal{U}$  are  $\bigcup_{i \in \omega} \mathcal{X}_i$  and  $\bigcup_{i \in \omega} \mathcal{U}_i$  respectively. We let  $\Theta_t$  be  $\{\alpha_{[t],[t'_1], \dots, [t'_\ell], q'} \mid (t t'_1 \dots t'_\ell, q') \in \mathcal{U}\}$  in Section 3.1.

**Lemma 5.**  $\Gamma \vdash_{\mathcal{B}} t : q$  holds for every  $(t, q) \in \mathcal{U}$ .

*Proof.* This follows from the properties that  $\Gamma \vdash_{\mathcal{B}} S : q_{\mathcal{B},0}$  and that  $\forall (t, q) \in \mathcal{U}. \Gamma \vdash_{\mathcal{B}} t : q$  implies  $\forall (t', q') \in \mathcal{U}_{i+1}. \Gamma \vdash_{\mathcal{B}} t' : q'$ . The latter can be shown by easy case analysis on the rule used for deriving  $(\mathcal{X}_i, \mathcal{U}_i) \rightarrow_{\sim} (\mathcal{X}_{i+1}, \mathcal{U}_{i+1})$ .

- Case for (R-CONST): It suffices to show that  $\Gamma \vdash_{\mathcal{B}} a t_1 \dots t_\ell : q$  and  $\delta(q, a) = q_1 \dots q_\ell$  imply  $\Gamma \vdash_{\mathcal{B}} t_i : q_i$  for every  $i$ , which follows immediately from the typing rules.
- Case for (R-FAIL): Trivial, as the reduction does not introduce any new pair of the form  $(t, q)$ . (Actually, (R-FAIL) is not applicable.)
- Case for (R-NT): This follows from the standard subject reduction property.
- Case for (R-EQ): It suffices to show that  $\Gamma \vdash_{\mathcal{B}} t t_1 \dots t_\ell : q$  and  $t \sim t'$  imply  $\Gamma \vdash_{\mathcal{B}} t' t_1 \dots t_\ell : q$ . By the assumption  $\sim \subseteq \sim_{\Gamma}$ , we have  $\Gamma \vdash_{\mathcal{B}} t : \tau$  if and only if  $\Gamma \vdash_{\mathcal{B}} t' : \tau$  for every  $\tau$ . Thus, a derivation for  $\Gamma \vdash_{\mathcal{B}} t' t_1 \dots t_\ell : q$  can be constructed from that of  $\Gamma \vdash_{\mathcal{B}} t t_1 \dots t_\ell : q$  by replacing the derivation of  $\Gamma \vdash_{\mathcal{B}} t : \tau$  with that of  $\Gamma \vdash_{\mathcal{B}} t' : \tau$ .

□

We say that a type derivation for  $\Gamma \vdash_{\mathcal{B}} t : \tau$  is *normal* if it is derivable by using only judgments of the form  $\Gamma' \vdash_{\mathcal{B}} u : (E, \alpha)$  where  $\alpha \in \Theta_u$ .

**Lemma 6.** If  $(t t_1 \dots t_\ell, q) \in \mathcal{U}$ , then there exists a normal derivation for  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} t : \bigwedge \Theta_{t_1} \rightarrow \dots \rightarrow \bigwedge \Theta_{t_\ell} \rightarrow q$ .

*Proof.* By induction on the structure of  $t$ .

- Case  $t = a$ : By Lemma 5,  $\Gamma \vdash_{\mathcal{B}} a t_1 \dots t_\ell : q$  holds, so that we have  $\delta(q, a) = q_1 \dots q_\ell$  with  $(t_i, q_i) \in \mathcal{U}$  for some  $q_1, \dots, q_\ell$ . Thus,  $q_i \in \Theta_{t_i}$  for each  $i$ . By the typing rule for constants, we have  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} a : \bigwedge \Theta_{t_1} \rightarrow \dots \rightarrow \bigwedge \Theta_{t_\ell} \rightarrow q$  as required. Furthermore,  $\bigwedge \Theta_{t_1} \rightarrow \dots \rightarrow \bigwedge \Theta_{t_\ell} \rightarrow q \in \Theta_{[a]}$ .
- Case  $t = F$ : This follows immediately from the definition of  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$ .
- Case  $t = s u$ : By induction hypothesis, we have a normal derivation for:

$$\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} s : \bigwedge \Theta_u \rightarrow \bigwedge \Theta_{t_1} \rightarrow \dots \rightarrow \bigwedge \Theta_{t_\ell} \rightarrow q.$$

For each  $\sigma_1 \rightarrow \dots \rightarrow \sigma_k \rightarrow q' \in \Theta_u$ , there must exist  $u_0, u_1, \dots, u_k$  such that:

$$\begin{aligned} (u_0 u_1 \dots u_k, q') &\in \mathcal{U} & u &\sim u_0 \\ \sigma_i &= \bigwedge \Theta_{u_i} \quad (1 \leq i \leq k) \end{aligned}$$

Thus, we have  $(u u_1 \dots u_k, q') \in \mathcal{U}$ . By induction hypothesis, we have a normal derivation for  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} u : \sigma_1 \rightarrow \dots \rightarrow \sigma_k \rightarrow q'$ . Therefore, we have a normal derivation for  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} s u : \bigwedge \Theta_{t_1} \rightarrow \dots \rightarrow \bigwedge \Theta_{t_\ell} \rightarrow q$  as required.  $\square$

*Proof of Theorem 4.* As  $S : q_{\mathcal{B}, 0} \in \Gamma_{\mathcal{X}, \mathcal{U}, \sim}$ , it suffices to show  $\vdash_{\mathcal{B}} \mathcal{G} : \Gamma_{\mathcal{X}, \mathcal{U}, \sim}$ . Suppose  $F : (E, \alpha) \in \Gamma_{\mathcal{X}, \mathcal{U}, \sim}$ , i.e.,  $\alpha \in \Theta_F$ , and  $\mathcal{R}(F) = \lambda x_1, \dots, x_\ell. u$ .

Then  $E(\alpha)$  is of the form  $\sigma_1 \rightarrow \dots \rightarrow \sigma_\ell \rightarrow q$ . By the definition of  $\Theta_F$ , there must exist  $u_0, u_1, \dots, u_\ell$  such that:

$$F \sim u_0 \quad (u_0 u_1 \dots u_\ell, q) \in \mathcal{U} \quad \sigma_i = \sigma_{[u_i]}$$

Thus, by the fairness of the reduction sequence, we have:  $(F u_1 \dots u_m, q) \in \mathcal{U}$ , which also implies:

$$([u_1/x_1, \dots, u_\ell/x_\ell]t, q).$$

By Lemma 6, we have a normal derivation for

$$\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} [u_1/x_1, \dots, u_\ell/x_\ell]t : q.$$

From the derivation of it, we obtain:

$$\Gamma_{\mathcal{X}, \mathcal{U}, \sim}, x : \sigma_{[u_1]}, \dots, x : \sigma_{[u_\ell]} \vdash_{\mathcal{B}} t : q,$$

by replacing each node of the form  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} u_i : \tau_i$  with  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}, x : \sigma_{[u_1]}, \dots, x : \sigma_{[u_\ell]} \vdash_{\mathcal{B}} x_i : \tau_i$ . Thus, we have  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} \lambda x_1, \dots, x_\ell. t : \tau$  as required.  $\square$

## A.2 Proof of Theorem 5

We first prove the following lemma.

**Lemma 7.** *Let  $(\mathcal{X}_0, \mathcal{U}_0)$  be  $(\{S\}, \{(S, q_{\mathcal{B}, 0})\})$ . Suppose that  $\sim'$  and  $\sim$  be equivalence relations on  $\mathbf{Tm}$  such that  $\sim' \subseteq \sim$ , and that*

$$\begin{aligned} (\mathcal{X}_0, \mathcal{U}_0) &\longrightarrow_{\sim} (\mathcal{X}_1, \mathcal{U}_1) \longrightarrow_{\sim} (\mathcal{X}_2, \mathcal{U}_2) \longrightarrow_{\sim} \dots, \text{ and} \\ (\mathcal{X}'_0, \mathcal{U}'_0) &\longrightarrow_{\sim'} (\mathcal{X}'_1, \mathcal{U}'_1) \longrightarrow_{\sim'} (\mathcal{X}'_2, \mathcal{U}'_2) \longrightarrow_{\sim'} \dots \end{aligned}$$

*are fair reduction sequences, with  $(\mathcal{X}'_0, \mathcal{U}'_0) = (\mathcal{X}_0, \mathcal{U}_0)$ . Then,  $\Gamma_{\bigcup_i \mathcal{X}'_i, \bigcup_i \mathcal{U}'_i, \sim'} \sqsubseteq \Gamma_{\bigcup_i \mathcal{X}_i, \bigcup_i \mathcal{U}_i, \sim}$ .*

*Proof.* By the condition  $\sim' \subseteq \sim$  and the fairness of the reductions, we have  $(\bigcup_i \mathcal{X}'_i) \subseteq (\bigcup_i \mathcal{X}_i)$  and  $(\bigcup_i \mathcal{U}'_i) \subseteq (\bigcup_i \mathcal{U}_i)$ . Thus, the required condition follows from the definition of  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$ .  $\square$

Theorem 5 follows as an immediate corollary of Theorem 4 and Lemma 7.

*Proof of Theorem 5.* Let  $\sim' = (\sim \cap \sim_\Gamma)$ , and

$$(\mathcal{X}'_0, \mathcal{U}'_0) \longrightarrow_{\sim'} (\mathcal{X}'_1, \mathcal{U}'_1) \longrightarrow_{\sim'} (\mathcal{X}'_2, \mathcal{U}'_2) \longrightarrow_{\sim'} \dots$$

be a fair reduction sequence with  $(\mathcal{X}'_0, \mathcal{U}'_0) = (\mathcal{X}_0, \mathcal{U}_0)$ . By Theorem 4, we have  $\Gamma' \vdash_B (\mathcal{G}, S_{\mathcal{G}}) : q_{B,0}$  for  $\Gamma' = \Gamma_{\cup_i \mathcal{X}'_i, \cup_i \mathcal{U}'_i, \sim'}$ . By Lemma 7, we have  $\Gamma' \sqsubseteq \Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  as required.  $\square$

## B Operational Semantics of Multi-Threaded Programs

We define the transition relation  $P \xrightarrow{\ell} P'$ , where  $\ell$  is either a global action or  $\epsilon$  (which represents an internal computation). It means that program  $P$  is reduced to  $P'$  in one step, with an action  $\ell$ . The set of values, ranged over by  $v$ , is given by:

$$v ::= () \mid \mathbf{fun}(f, x, M)$$

The relation  $P \xrightarrow{\ell} P'$  is inductively defined by:

$$\frac{}{(\mathbf{fun}(f, x, M))V \xrightarrow{\epsilon} [\mathbf{fun}(f, x, M)/f, V/x]M}$$

$$\frac{i \in \{1, 2\}}{M_1 \square M_2 \xrightarrow{\epsilon} M_i}$$

$$\frac{M \xrightarrow{\ell} M'}{MN \xrightarrow{\ell} M'N}$$

$$\frac{N \xrightarrow{\ell} N'}{MN \xrightarrow{\ell} MN'}$$

$$\frac{M \xrightarrow{\ell} M'}{M \parallel N \xrightarrow{\ell} M' \parallel N}$$

$$\frac{N \xrightarrow{\ell} N'}{M \parallel N \xrightarrow{\ell} M \parallel N'}$$

We write  $M \xrightarrow{a_1 \dots a_n} N$  if:

$$M(\xrightarrow{\epsilon})^* \xrightarrow{a_1} (\xrightarrow{\epsilon})^* \dots (\xrightarrow{\epsilon})^* \xrightarrow{a_n} (\xrightarrow{\epsilon})^* N.$$