

# Automatically Disproving Fair Termination of Higher-Order Functional Programs

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## Abstract

We propose an automated method for *disproving* fair termination of higher-order functional programs, which is complementary to Murase et al.'s recent method for *proving* fair termination. A program is said to be *fair terminating* if it has no infinite execution trace that satisfies a given fairness constraint. Fair termination is an important property because program verification problems for arbitrary  $\omega$ -regular temporal properties can be transformed to those of fair termination. Our method reduces the problem of disproving fair termination to higher-order model checking by using predicate abstraction and CEGAR. Given a program, we convert it to an abstract program that generates an approximation of the (possibly infinite) execution traces of the original program, so that the original program has a fair infinite execution trace if the tree generated by the abstract program satisfies a certain property. The method is a non-trivial extension of Kuwahara et al.'s method for disproving plain termination. We implemented a prototype verification tool based on our method and confirmed its effectiveness.

**Categories and Subject Descriptors** D.2.4 [Software Engineering]: Software/Program Verification—Formal methods, Model checking; F.3.1 [Logics and Meaning of Programs]: Specifying and Verifying and Reasoning about Programs—Mechanical verification

**General Terms** Languages, Verification

**Keywords** Automatic Verification, Fair Termination, Higher-Order Programs, Predicate Abstraction, Higher-Order Model Checking, CEGAR

## 1. Introduction

There has recently been rapid progress in automated (or semi-automated) techniques for verification of higher-order functional programs [11, 15–17, 19, 21, 22, 26, 27, 29, 30, 33, 35, 36]. Verification methods have been proposed for various properties, including safety properties [19, 33], termination [11, 21], non-termination [22], and fair termination [24].

In the present paper, we propose a method for *disproving* fair termination, which plays a role complementary to Murase et al.'s method [24] for proving fair termination. Fair termination is a

key to proving temporal properties of programs: Vardi [34] has shown that the verification of any  $\omega$ -regular temporal property can be reduced to that of fair termination. Cook et al. [4] applied the reduction to verification of liveness properties of imperative programs. Murase et al. [24] has recently proposed a method for *proving* fair termination of higher-order programs, but their method cannot be used for *disproving* it.

Let us briefly explain the notion of fair termination. A (possibly infinite) execution trace  $\sigma$  of a program is *fair* with respect to a (Streett) fairness constraint  $\{(A_1, B_1), \dots, (A_n, B_n)\}$  (where  $A_i, B_i$  are called *events*) when, for every  $i$ , if the event  $A_i$  occurs infinitely often in  $\sigma$ , so does the event  $B_i$ . For example, an infinite trace  $(AB)^\omega = ABABAB \dots$  is fair with respect to the constraint  $\{(A, B)\}$ , but  $A^\omega = AAA \dots$  is not. A program  $P$  is *fair terminating* when  $P$  has no fair infinite execution traces. Many temporal properties can be naturally reduced to fair termination. For example, let *Never* be an event that never happens. Then, the fair termination of a program with respect to the constraint  $\{(A, \text{Never})\}$  means that  $A$  occurs infinitely often in any infinite run of the program; notice that an infinite run that contains only finitely many  $A$ 's is fair, violating the fair termination condition. For another example, to check that a function  $f$  is eventually called in any infinite run of the program, it suffices to replace the function  $f$  with a function to raise event  $A$  infinitely often, and check that the resulting program is fair terminating with respect to  $\{(A, \text{Never})\}$ . See [34] for a general reduction from  $\omega$ -regular property verification to fair termination verification.

As a concrete example for illustrating why fair termination is of interest, let us consider the following program  $P_0$ , consisting of mutually recursive function definitions:

```
rand () = *int
randneg () = let x = rand () in
              if 0 ≤ x then x else randneg ()
main = randneg ().
```

Here,  $*_{\text{int}}$  is a special expression that generates a random integer; thus the function *rand* returns a random integer. The function *randneg* returns a non-negative random integer, by repeatedly calling *rand* and then returning the result only if it is non-negative.

Suppose that we wish to prove that the program above is terminating; note that the program is terminating indeed, since *rand* must eventually return a non-negative integer by the assumption on randomness. Ordinary termination verification methods [11, 21] would, however, fail to prove so, since they would treat  $*_{\text{int}}$  as a non-deterministic function to return an integer. Using fair termination, we can incorporate the assumption that *rand* eventually returns a non-negative integer. Let us instrument the program above,

as the following program  $P_1$ :

```

rand () = let r = *int in
  if 0 ≤ r then (event B; r) else (event A; r)
randneg () = let x = rand () in
  if 0 ≤ x then x else randneg ()
main = randneg ().

```

Here, we have inserted event expressions (**event**  $B$  and **event**  $A$ ), to signal whether the value returned by  $rand$  is non-negative or not. Then, the fairness assumption about  $rand$  can be expressed by  $\{(A, B)\}$ ,<sup>1</sup> and fair termination of the program under  $\{(A, B)\}$  means that the program terminates if  $rand$  is fair. The program  $P_1$  above is indeed fair terminating under  $\{(A, B)\}$ , and can be proved so by the method of Murase et al. [24].

Now we consider the following program  $P_2$ , which is a variation of  $P_1$ :

```

rand () = let r = *int in
  if 0 ≤ r then (event B; r) else (event A; r)
randpos () = let x = rand () in
  if 0 < x then x else randpos ()
main = randpos ().

```

Notice that the branching condition in the second function has been changed to  $0 < x$ . The program is not fair terminating under  $\{(A, B)\}$ . If  $rand$  returns 0 and a negative integer alternately, the event sequence is  $BABABA \dots$ , which is fair, but the program is not terminating.

As already mentioned, the goal of the present paper is to automatically disprove fair termination (i.e., to prove that a given program has a fair infinite execution trace). For the examples above, we wish to show that  $P_2$  is *not* fair terminating under  $\{(A, B)\}$ . In general, disproving a liveness property is much harder than disproving a safety property. In the case of safety, one just needs to find a *finite* execution trace that violates the safety property. In contrast, in the case of liveness, we need to find an *infinite* execution trace that violates the property. Even for plain termination, it is only recent that automated methods for disproving termination [11, 22] have been obtained for higher-order functional programs.

Following Kuwahara et al.’s method for disproving *plain* termination [22], we combine higher-order model checking [28] with predicate abstraction and counterexample-guided abstraction refinement (CEGAR) to disprove fair termination. Higher-order model checking decides whether the (possibly infinite) tree generated by a given higher-order tree grammar (called a higher-order recursion scheme; HORS for short) satisfies a given regular property. For *finite data* higher-order programs where only finite base types (such as Booleans, not infinite data domains like integers and lists) are allowed, arbitrary regular properties can be decided by first transforming a program to a HORS, and then applying higher-order model checking [17, 23]. For *infinite data* programs (that may use data from infinite data domains, like integers, lists, and trees), however, we need to combine higher-order model checking with predicate abstraction.

An overall flow of our method is shown in Figure 1. In Step 1, given a higher-order functional program  $P$  (that may use infinite data domains) and a fairness constraint  $C$  as input, we apply predicate abstraction to generate a pair consisting of (i) an abstract program, which is a *Boolean*, higher-order, *tree-generating* functional program  $Q_P$ , and (ii) a tree automaton  $\mathcal{A}_C$ , which describes the

<sup>1</sup>This actually defines a stronger assumption than needed here, that if  $rand$  returns negative integers infinitely often, then it also returns non-negative integers *infinitely often*, not just once.

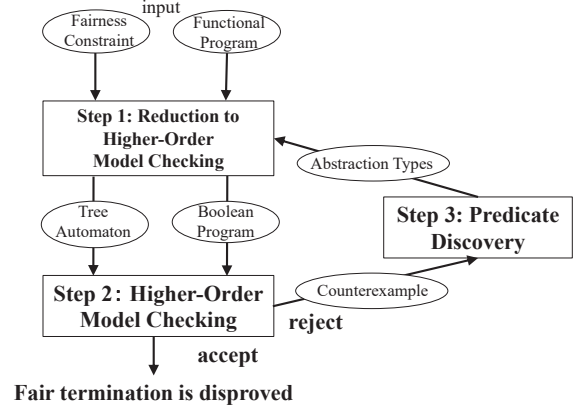


Figure 1: Overview of method.

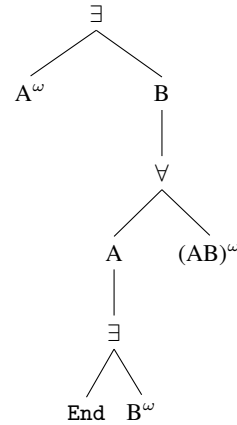


Figure 2: An example of the tree generated by an abstract program.

property that should be satisfied by the tree generated by  $Q_P$ . The tree generated by  $Q_P$  describes an abstraction of possible execution traces of the original program  $P$ . Figure 2 shows an example of the tree generated by an abstract program. The tree consists of event nodes  $A$  and  $B$ , and special nodes  $\exists$ ,  $\forall$ , and  $\text{End}$ . In the figure,  $A^\omega$  abbreviates an infinite tree path where  $A$  occurs consecutively, and  $(AB)^\omega$  abbreviates an infinite path where  $A$  and  $B$  occur alternately. A  $\exists$ -node means that for every branch of the node, there is a corresponding execution of the original program, while a  $\forall$ -node means that for at least one of the branches, there is a corresponding execution of the original program (the other branches may be spurious, introduced by abstraction). An  $\text{End}$ -node describes the termination. Thus, to prove that the original program has a fair infinite execution trace (which is the goal of our method), it suffices to show, roughly speaking, (i) for each  $\exists$ -node, there exists a fair infinite path, and (ii) for each  $\forall$ -node, every branch has a fair infinite path. These conditions are formally described by the tree automaton  $\mathcal{A}_C$ . The tree in Figure 2 indeed satisfies the property mentioned above; if we choose the right branches for both  $\exists$ -nodes, then we get two infinite paths  $B(AB)^\omega$  and  $BAB^\omega$ , which are both fair under  $\{(A, B)\}$ .

To see how an abstract program may be constructed, consider the following program  $P_3$ , which is a continuation passing style

(CPS) version of program  $P_2$  above:

```

rand k = f *int k
f r k = if 0 ≤ r then (event B; k r)
      else (event A; k r)
randpos k = rand (λx. if 0 < x then k x else randpos k)
main = randpos λx.x.

```

Suppose that we have chosen the predicate  $0 < n$  for abstracting every integer  $n$ . Then we get the following abstract program  $Q_{P_3}$ :

```

rand k = ∃(f false k, f true k)
f b0<r k = if b0<r then B(k(b0<r))
          else ∀(A(k(b0<r)), B(k(b0<r)))
randpos k = rand (λb0<x.
  if b0<x then k b0<x else randpos k)
main = randpos (λb0<x. End).

```

Here, the integer variables  $r$  and  $x$  have been replaced by Boolean variables  $b_{0<r}$  and  $b_{0<x}$ , which respectively represent whether the values of  $r$  and  $x$  in the original program are positive or not. Event expressions have been replaced by unary tree constructors  $A$  and  $B$ . In addition, we have inserted tree constructors  $\exists$  and  $\forall$ . In the body of  $rand$  in  $P_3$ , the value returned by  $*_{int}$  may be positive or not. Thus, in  $Q_{P_3}$ , we create two subtrees for  $f \text{ false } k$  and  $f \text{ true } k$ , which describe executions for the cases where negative and non-negative values have been returned respectively; we then combine them with  $\exists$ , to indicate that, to prove that the program has a fair infinite execution, it suffices to show that one of the subtrees has a fair infinite path. In the body of  $f$  in  $Q_{P_3}$ , the conditional branch on  $r$  in the original program has been replaced by that on  $b_{0<r}$ . If  $b_{0<r}$  is **true**, then the original condition  $0 \leq r$  is also **true**, so we create a tree describing the computation of the then-branch of the original program. If  $b_{0<r}$  is **false**, then the original condition  $0 \leq r$  may be **true** or **false**; thus we create two subtrees for describing both cases, and combine them with a  $\forall$ -node, to indicate that, to prove that the program has a fair infinite execution, we need to show that *both subtrees* have a fair infinite path (since, due to the abstraction, we do not know which subtree describes the actual computation of the original program). The tree generated by the abstract program is shown in Figure 3, where the two  $\exists$ -subtrees at the bottom are identical to the whole tree; so it is the regular tree  $X$  defined by:<sup>2</sup>

$$X = \exists(\forall(A(X), B(X)), B(\text{End})).$$

In Step 2, we use higher-order model checking to check whether the tree generated by the abstract program  $Q_P$  is accepted by the tree automaton  $A_C$ ; recall that  $A_C$  describes a sufficient condition for the original program  $P$  to have a fair non-terminating execution trace. The tree generated by the abstract program  $Q_{P_3}$  given above actually does not satisfy the sufficient condition. To show the existence of a fair non-terminating execution, for each  $\exists$ -node, we have to choose the left branch; however, if we choose the left branch of every  $\forall$ -node, we get an event sequence  $A^\omega = AAA \dots$ , which is unfair with respect to  $\{(A, B)\}$ . In such a case, we let a higher-order model checker output a counterexample, which is a subtree of the tree generated by  $Q_P$ , showing why the tree is not accepted by  $A_C$ . Actually, since such a subtree is infinite in general, we generate a (higher-order) tree grammar that generates the subtree as a counterexample, and pass it to Step 3. For the example above, the

<sup>2</sup>The tree generated by an abstract program is not necessarily regular in general.

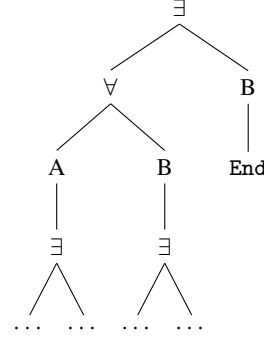


Figure 3: The tree generated by  $Q_{P_3}$ .

counterexample can be described by the grammar:

$$X = \exists(\forall(A(X), -), B(\text{End})).$$

In Step 3, we analyze the counterexample, and if it is spurious, discover predicates to refine the abstraction. This may sound standard, but it is actually non-trivial, since the counterexample is an *infinite* tree and it has two kinds of branching nodes  $\forall$  and  $\exists$ .

For the example above, suppose that a new predicate  $0 \leq r$  has been discovered in Step 3. Then we go back to Step 1, and obtain the following refined version of the abstract program:

```

rand k =
  ∃(f(false, false) k, f(true, false) k, f(true, true) k)
f(b1, b2) k = if b1 then B(k(b1, b2)) else A(k(b1, b2))
randpos k = rand (λ(b1, b2).
  if b2 then k(b1, b2) else randpos k)
main = randpos (λx. End).

```

Now each integer value has been abstracted to a *pair* of Booleans  $b_1, b_2$ , which represent whether the original value  $r$  satisfies  $0 \leq r$  and  $0 < r$  respectively. The tree generated by the new abstract program is the regular tree  $Y$  defined by:

$$Y = \exists(A(Y), B(Y), B(\text{End})).$$

By choosing the second branch of each  $\exists$ -node, we get an infinite event sequence  $B^\omega = BBB \dots$  that is fair under  $\{(A, B)\}$ . Thus, we can finally conclude that the original program  $P_3$  is *not* fair terminating under  $\{(A, B)\}$ .

The overall flow outlined above is similar to Kuwahara et al.'s method [22] for disproving plain termination. Each step, however, requires non-trivial extensions of their method.

- First, in Step 1, to describe the condition required for the tree generated by an abstract program, we need alternating parity tree automata (APT; or equi-expressive automata like Streett automata); in the case of Kuwahara et al.'s method [22], (co-)trivial automata [1] were sufficient. As a consequence, in Step 2, we need a higher-order model checker for the full class of APT [10, 25]; in Kuwahara et al.'s method, a trivial automata model checker was sufficient. To our knowledge, this is the first serious application of higher-order model checking for the full class of APT to verification of infinite-data higher-order programs.
- Second, as explained through the example, we need a higher-order model checker capable of generating a higher-order tree grammar as a description of a counterexample. Unfortunately, the existing higher-order model checkers for APT did not have this feature. We have applied a technique called *effective selection* [3, 13, 32] and implemented the counterexample genera-

$$\begin{aligned}
P \text{ (programs)} &::= \{f_i \tilde{x}_i = e_i\}_{i \in \{1 \dots n\}} \\
e \text{ (expressions)} &::= () \mid \mathbf{event} A; e \mid y \tilde{v} \\
&\quad \mid \mathbf{if} a \text{ then } e_1 \mathbf{else} e_2 \\
&\quad \mid \mathbf{let} x = a \mathbf{in} e \mid \mathbf{let} x = *_{\text{int}} \mathbf{in} e \\
a \text{ (simple expressions)} &::= n \mid x \mid \mathbf{op}(\tilde{a}) \\
v \text{ (values)} &::= n \mid y \tilde{v} \\
\\
\frac{f \tilde{x} = e \in P \quad |\tilde{x}| = |\tilde{v}|}{f \tilde{v} \xrightarrow{\epsilon}_{\rightarrow P} [\tilde{v}/\tilde{x}]e} & \quad (\text{E-APP}) \\
\\
\frac{\llbracket a \rrbracket = n}{\mathbf{let} x = a \mathbf{in} e \xrightarrow{\epsilon}_{\rightarrow P} [n/x]e} & \quad (\text{E-LET}) \\
\\
\frac{n \neq 0}{\mathbf{if} n \text{ then } e_1 \mathbf{else} e_2 \xrightarrow{\epsilon}_{\rightarrow P} e_1} & \quad (\text{E-IFT}) \\
\\
\mathbf{if} 0 \text{ then } e_1 \mathbf{else} e_2 \xrightarrow{\epsilon}_{\rightarrow P} e_2 & \quad (\text{E-IFF}) \\
\\
\mathbf{let} x = *_{\text{int}} \mathbf{in} e \xrightarrow{\epsilon}_{\rightarrow P} [n/x]e & \quad (\text{E-RAND}) \\
\\
\mathbf{event} A; e \xrightarrow{A}_{\rightarrow P} e & \quad (\text{E-EV})
\end{aligned}$$

Figure 4: The syntax and operational semantics of the source language.

tion feature. The technique of effective selection has only been investigated in a theoretical community (without much consideration on the practicality); ours is the first realistic implementation of effective selection.

- Third, in Step 3, we need to discover predicates from (grammar descriptions of) *infinite* counterexample trees; In Kuwahara et al.’s method, counterexamples were finite trees.

We have formalized the overall method, implemented an automated tool for disproving fair termination, and confirmed the effectiveness of the approach through experiments.

The rest of this paper is structured as follows. Section 2 introduces the language used as the target of our verification method. Sections 3–5 describe each step of our method. Section 6 reports implementation and experiments. Section 7 discusses related work and Section 8 concludes the paper.

## 2. Language

This section defines the target language of our verification method, and defines the notion of fair termination formally.

The language is a simply typed, call-by-value higher-order functional language. The syntax of the language is shown in Figure 4. In the figure,  $\tilde{x}$  abbreviates a sequence  $x_1, \dots, x_k$  (similarly for  $\tilde{v}$  and  $\tilde{a}$ ); we write  $|\tilde{x}|$  for the length of the sequence  $\tilde{x}$ . A program consists of a finite set of mutually recursive function definitions. We assume that there exists  $i$  such that  $f_i = \mathbf{main}$ , which is the “main” function, and  $|\tilde{x}_i| = 0$ . We call  $|\tilde{x}_i|$  the arity of function  $f_i$ . The expression  $()$  represents the unit value. For the sake of simplicity, we assume that programs are represented in the CPS style; thus every function returns the unit value. The expression  $\mathbf{event} A; e$  raises an event  $A$ , and then evaluates  $e$ . We assume a

finite set of events, and use meta-variables  $A$  and  $B$  for them. Event expressions are just used for signaling that certain control points have been reached; as demonstrated in Section 1, they are useful for specifying program properties. The expression  $y \tilde{v}$  represents a function call, where  $y$  may be a variable or a function name  $f_i$ . The value  $v_i$  that may be passed to  $y$  is either an integer, a variable (the special case of  $y \tilde{v}$  where  $|\tilde{v}| = 0$ ), or a partial application (thus,  $|\tilde{v}|$  must be smaller than the arity of  $y$ ). The conditional expression  $\mathbf{if} a \text{ then } e_1 \mathbf{else} e_2$  evaluates  $e_2$  if the value of  $a$  is 0, and  $e_1$  otherwise. We sometimes write  $\mathbf{true}$  for 1 and  $\mathbf{false}$  for 0. The meta-variable  $a$  ranges over the set of *simple* expressions, which consists of integers (ranged over by the meta-variable  $n$ ), variables, and integer operators. The meta-variable  $\mathbf{op}$  ranges over a set of integer operators; we assume that we have standard primitives like  $+$ ,  $=$ ,  $<$ , and  $\leq$  (where  $=$ ,  $<$ ,  $\leq$  are assumed to return 0 or 1, based on the interpretation of Booleans above). The expression  $\mathbf{let} x = *_{\text{int}} \mathbf{in} e$  picks an integer in a non-deterministic manner, binds  $x$  to it, and evaluates  $e$ .<sup>3</sup>

The operational semantics of the language is given via a labeled reduction relation  $e \xrightarrow{l}_{\rightarrow P} e'$ , which is defined in Figure 4. In the figure,  $\llbracket a \rrbracket$  stands for the value of the simple expression  $a$ . Label  $l$  is either an event symbol or the empty sequence  $\epsilon$ . We write  $e \xrightarrow{s}_{\rightarrow P}^* e'$  if  $e \xrightarrow{l_1}_{\rightarrow P} \dots \xrightarrow{l_n}_{\rightarrow P} e'$  with  $s = l_1 \dots l_n$ . We omit the subscript  $P$  if it is clear from the context.

We consider only well-typed programs. The syntax of types and the typing rules are given in Figure 5. The type  $\mathbf{int}$  and  $\star$  describe integers and the unit value, respectively. The type  $\tau_1 \rightarrow \tau_2$  describes functions from  $\tau_1$  to  $\tau_2$ . We assume  $\tau_1 \neq \star$  in  $\tau_1 \rightarrow \tau_2$ . In typing rules, we abbreviate  $\tau_1 \rightarrow \dots \rightarrow \tau_n \rightarrow \star$  and  $\tilde{x}_1 : \tilde{\tau}_1, \dots, \tilde{x}_n : \tilde{\tau}_n \rightarrow \tilde{\tau} \rightarrow \star$  and  $\tilde{x}_i : \tilde{\tau}_i$ , respectively. The typing rules are standard, except that the body of each function definition must have type  $\star$ . This requirement is imposed for the sake of convenience for formalizing the predicate abstraction in the next section. Note that we do not lose generality because we can apply continuation-passing-style (CPS) transformation to ensure that every function satisfies the requirement; the type  $\star$  corresponds to the answer type in CPS. These restrictions are just for the technical convenience for formalizing the method; the actual implementation reported in Section 6 does not impose such restrictions.

**Example 1.** The program  $P_3$  in Section 1 is expressed as the following program  $P'_3$  in our language:

$$\begin{aligned}
\mathbf{rand} k &= \mathbf{let} r = *_{\text{int}} \mathbf{in} f r k \\
f r k &= \mathbf{if} 0 \leq r \text{ then } (\mathbf{event} B; k r) \mathbf{else} (\mathbf{event} A; k r) \\
\mathbf{randpos} k &= \mathbf{rand} (g k) \\
g k x &= \mathbf{if} 0 < x \text{ then } k x \mathbf{else} \mathbf{randpos} k \\
\mathbf{main} &= \mathbf{randpos} h \\
h x &= ().
\end{aligned}$$

For readability, we sometimes use  $\lambda$ -abstractions (as in Section 1). The main function  $\mathbf{main}$  is reduced as follows.

$$\begin{aligned}
\mathbf{main} &\xrightarrow{\epsilon}_{\rightarrow P'_3} \mathbf{randpos} h \xrightarrow{\epsilon}_{\rightarrow P'_3} \mathbf{rand} (g h) \xrightarrow{\epsilon}_{\rightarrow P'_3} f 0 (g h) \\
&\xrightarrow{B}_{\rightarrow P'_3} g h 0 \xrightarrow{\epsilon}_{\rightarrow P'_3} \mathbf{randpos} h \xrightarrow{B}_{\rightarrow P'_3} \mathbf{randpos} h \xrightarrow{B}_{\rightarrow P'_3} \dots
\end{aligned}$$

Thus, the program has an infinite execution sequence labeled by  $B^\omega = BBB \dots$ .  $\square$

We now define the notion of fair termination.

<sup>3</sup>Thus, contrary to the explanation in Section 1, we do not assume the randomness or fairness of  $*_{\text{int}}$  a priori. That  $*_{\text{int}}$  returns values in a fair manner should be declared using events and fairness constraints as done in Section 1.

Types:

$$\tau ::= \mathbf{int} \mid \star \mid \tau_1 \rightarrow \tau_2$$

Typing for expressions:

$$\frac{}{\Delta \vdash_{\text{ST}} () : \star}$$

$$\frac{\Delta \vdash_{\text{ST}} e : \star}{\Delta \vdash_{\text{ST}} \mathbf{event} A; e : \star}$$

$$\frac{\Delta(y) = \tau_1 \rightarrow \dots \rightarrow \tau_k \rightarrow \tau \quad \Delta \vdash_{\text{ST}} v_i : \tau_i \quad (\text{for all } i \in \{1, \dots, k\})}{\Delta \vdash_{\text{ST}} y v_1 \dots v_k : \tau}$$

$$\frac{\Delta \vdash_{\text{ST}} a : \mathbf{int} \quad \Delta \vdash_{\text{ST}} e_1 : \star \quad \Delta \vdash_{\text{ST}} e_2 : \star}{\Delta \vdash_{\text{ST}} \mathbf{if} a \mathbf{then} e_1 \mathbf{else} e_2 : \star}$$

$$\frac{\Delta \vdash_{\text{ST}} a : \mathbf{int} \quad \Delta, x : \mathbf{int} \vdash_{\text{ST}} e : \star}{\Delta \vdash_{\text{ST}} \mathbf{let} x = a \mathbf{in} e : \star}$$

$$\frac{\Delta, x : \mathbf{int} \vdash_{\text{ST}} e : \star}{\Delta \vdash_{\text{ST}} \mathbf{let} x = *_{\text{int}} \mathbf{in} e : \star}$$

Typing for simple expressions:

$$\Delta \vdash_{\text{ST}} n : \mathbf{int}$$

$$\frac{\Delta \vdash_{\text{ST}} a_i : \mathbf{int} \quad (\text{for each } i \in \{1, \dots, \text{ar}(\mathbf{op})\})}{\Delta \vdash_{\text{ST}} \mathbf{op}(a_1, \dots, a_k) : \mathbf{int}}$$

Typing for programs:

$$\frac{\Delta = f_1 : \tilde{\tau}_1 \rightarrow \star, \dots, f_k : \tilde{\tau}_k \rightarrow \star \quad \Delta(\mathbf{main}) = \star \quad \Delta, \tilde{x}_i : \tilde{\tau}_i \vdash_{\text{ST}} e_i : \star \quad (\text{for each } i \in \{1, \dots, k\})}{\vdash_{\text{ST}} \{f_1 \tilde{x}_1 = e_1, \dots, f_k \tilde{x}_k = e_k\} : \Delta}$$

Figure 5: Simple type system of the source language.

**Definition 2.1** (Fairness constraints). A (Streett) fairness constraint  $\mathcal{C}$  is a set of event pairs  $\{(A_1, B_1), \dots, (A_n, B_n)\}$ .

**Definition 2.2** (Fairness). Let  $\mathcal{C}$  be a fairness constraint and  $s$  be a possibly infinite sequence of events. The sequence  $s$  is *fair* with respect to  $\mathcal{C}$  when, for every  $(A_i, B_i) \in \mathcal{C}$ , if  $A_i$  occurs infinitely often in  $s$ , then so does  $B_i$ .

**Definition 2.3** (Fair Termination). Let  $\mathcal{C}$  be a fairness constraint and  $P$  be a program.  $P$  is *fair terminating* under  $\mathcal{C}$  if there exists no infinite reduction sequence

$$\mathbf{main} \xrightarrow{l_1}_P e_1 \xrightarrow{l_2}_P e_2 \xrightarrow{l_3}_P \dots,$$

such that the event sequence  $l_1 l_2 l_3 \dots$  is fair with respect to  $\mathcal{C}$ .

In other words, a program is fair terminating if every non-terminating reduction sequence is unfair. Note that plain termination is a special case of fair termination, where the fairness constraint  $\mathcal{C}$  is empty.

**Example 2.** Recall the program  $P'_3$  in Example 1. It is *not* fair terminating under  $\{(A, B)\}$  because it has a fair infinite reduction

sequence:

$$\mathbf{main} \xrightarrow{B}_{P'_3}^* \mathbf{randpos} h \xrightarrow{B}_{P'_3}^* \mathbf{randpos} h \xrightarrow{B}_{P'_3}^* \dots$$

Let  $P_4$  be the program obtained from  $P'_3$  by replacing the condition  $0 < x$  in the function  $g$  with  $0 \leq x$ .  $P_4$  is fair terminating, as every infinite reduction sequence must be of the form:

$$\mathbf{main} \xrightarrow{A}_{P_4}^* \mathbf{randpos} h \xrightarrow{A}_{P_4}^* \mathbf{randpos} h \xrightarrow{A}_{P_4}^* \dots,$$

which is not fair.  $\square$

The goal of the present paper is to develop an automated method for disproving the fair termination of a given program under a given fairness constraint. In other words, we develop a method for verifying that a given program has a fair infinite reduction sequence.

### 3. Step 1: Reduction to Higher-Order Model Checking

The task of Step 1 is to convert a given program  $P$  and a fairness constraint  $\mathcal{C}$  to an abstract program  $D_P$  and a tree automaton  $\mathcal{A}_{\mathcal{C}}$  respectively. The former generates a tree that abstracts possible execution traces of the program, and the latter describes a sufficient condition on the tree generated by  $D_P$  in order for  $P$  to have a fair non-terminating execution trace. The construction of  $D_P$  is actually almost the same as Kuwahara et al.'s work [22], except that we have event primitives. To make the paper self-contained, however, we briefly describe it in Section 3.1. The construction of  $\mathcal{A}_{\mathcal{C}}$  is new, which is described in Section 3.2.

#### 3.1 Construction of the Abstract Program $D_P$

In addition to a source program, we assume that we are given predicates used for abstracting each integer. They are supplied by Step 3; initially, the set of predicates is empty. The predicates used for abstraction are actually specified in the form of types called *abstraction types*, so that we can use different predicates for each position in the source program. The set of abstraction types is given by:

$$\begin{aligned} Q \text{ (abstraction types)} &::= \star \mid \mathbf{int}[Q_1, \dots, Q_k] \mid x : \sigma_1 \rightarrow \sigma_2 \\ Q \text{ (predicates)} &::= \lambda x. \varphi \\ \varphi \text{ (formulas)} &::= n_1 x_1 + \dots + n_k x_k \leq n \mid \varphi_1 \vee \varphi_2 \mid \neg \varphi. \end{aligned}$$

The type  $\star$  is the unit type and, the type  $\mathbf{int}[Q_1, \dots, Q_k]$  describes integers  $n$  that should be abstracted to a tuple of Booleans  $(Q_1(n), \dots, Q_k(n))$ . For example, the integer 3 is abstracted by the abstraction type  $\mathbf{int}[\lambda x. x = 0, \lambda x. x > 0]$  to  $(\mathbf{false}, \mathbf{true})$ . The dependent function type  $x : \sigma_1 \rightarrow \sigma_2$  describes functions that takes a value  $x$  of type  $\sigma_1$  and returns a value of type  $\sigma_2$ . When  $x$  does not occur in  $\sigma_2$ , we just write  $\sigma_1 \rightarrow \sigma_2$ . We assume that, along with a program  $P$ , we are given an abstraction type  $\sigma_i$  for each function  $f_i$  in  $P$ . For example, for the program  $P'_3$  in Example 1, we may be given the following types for functions:

$$\begin{aligned} \mathbf{rand} &: (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \star \\ f &: \mathbf{int}[\lambda x. 0 < x] \rightarrow (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \star \\ \mathbf{randpos} &: (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \star \\ g &: (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \mathbf{int}[\lambda x. 0 < x] \rightarrow \star \\ \mathbf{main} &: \star \\ h &: \mathbf{int}[\lambda x. 0 < x] \rightarrow \star, \end{aligned}$$

which specifies that every integer should be abstracted using the predicate  $\lambda x. 0 < x$ .

We use the language given in Figure 6 for describing abstract programs. It is a simply-typed, higher-order functional language having Booleans, tuples, and tree constructors as primitive data constructors. As in the source language, a program consists of a

finite set of mutually recursive function definitions; we assume it contains a definition of the main function **main**. The language has five kinds of tree constructors:  $A$  (for each event name  $A$ ), **Call**, **End**,  $\forall_i$ , and  $\exists_i$ . The constructor  $A$  and **Call** are unary; they are used to express the occurrences of event  $A$  and a function call respectively. The constructor **End** is nullary, and used to express the termination of the source program  $P$ . The  $i$ -ary constructor  $\forall_i$  is used to express that only one of the branches is guaranteed to correspond to an execution of the source program  $P$ ; the other branches may be spurious. Thus, for the purpose of proving the existence of a fair infinite execution, it suffices to show that *all* the branches have fair infinite paths, hence the name of the tree constructor “ $\forall$ ”. On the other hand, the  $i$ -ary constructor  $\exists_i$  is used to express that every branch corresponds to an execution of the source program  $P$ . Thus, it suffices to show that *one of* the branches has a fair infinite path.

The expression  $\forall\{\psi_1 \rightarrow M_1, \dots, \psi_k \rightarrow M_k\}$  ( $\exists\{\psi_1 \rightarrow M_1, \dots, \psi_k \rightarrow M_k\}$ , resp.) generates a node with label  $\forall$  ( $\exists$ , resp.), and for each  $i \in \{1, \dots, k\}$ , evaluates  $\psi_i$  and add the tree generated by  $M_i$  as a child of the node only if the value of  $\psi_i$  is **true**. The Boolean expression  $\#_i(x)$  is the  $i$ -th projection of the tuple  $x$ . For example, if  $x_1 = (\mathbf{true})$  and  $x_2 = (\mathbf{true}, \mathbf{false})$ , then  $\forall\{\#_1(x_1) \rightarrow A(\mathbf{End}), \#_2(x_2) \rightarrow B(\mathbf{End}), \#_1(x_1) \vee \#_2(x_2) \rightarrow \mathbf{End}\}$  evaluates to  $\forall_2(A(\mathbf{End}), \mathbf{End})$ . When  $x$  is a singleton tuple, we often omit  $\#_1$  and just write  $x$  for  $\#_1(x)$ . The meaning of the other expressions should be clear.

The construction of  $D_P$  is formalized as the type-based transformation relations  $\Gamma \vdash e : \sigma \rightsquigarrow M$  and  $\vdash P : \Gamma \rightsquigarrow D$  defined in Figure 7. The former means that the expression  $e$  in the source language can be abstracted to the expression  $M$  under the assumption that each free variable  $x$  is abstracted according to the abstraction type  $\Gamma(x)$ , and the latter means that the source program  $P$  should be abstracted to  $D$  under the assumption that each function  $f$  should be abstracted according to the abstraction type  $\Gamma(f)$ . In the figure,  $\models \varphi$  means that  $\varphi$  is a tautology. The expression  $b_i Q_i(x)$  stands for  $Q_i(x)$  if  $b_i$  is **true** and  $\neg Q_i(x)$  otherwise. The expression  $\theta_\Gamma$  represents the substitution that replaces each variable  $x$  of type  $\mathbf{int}[Q_1, \dots, Q_n]$  in  $\Gamma$  with  $(Q_1(x), \dots, Q_n(x))$ . For example, let  $\Gamma$  be  $x : \mathbf{int}[\lambda x. x \geq 0], y : \mathbf{int}[\lambda y. y = 0, \lambda y. y > x]$ , then  $\theta_\Gamma(\#_1(x) \vee \#_2(y)) = \#_1(x \geq 0) \vee \#_2(y = 0, y > x) = x \geq 0 \vee y > x$ .

The rule PA-EVENT just turns an event constructor  $A$  into the tree constructor. Since the other transformation rules are the same as Kuwahara et al.’s ones [22], we briefly explain only the key rules: PA-IF and PA-RAND. The rule PA-IF *over*-approximates the computation of a source program, since, due to an abstraction, we may not know which branch is actually taken. For example, consider the conditional expression **if**  $0 < x$  **then**  $e_1$  **else**  $e_2$ , and suppose that the abstraction type of  $x$  is  $\mathbf{int}[\lambda x. 0 \leq x]$ . Then, if the abstraction of  $x$  is **false**, we know that  $0 < x$  is **false**, so that only the else branch is taken. However, if the abstraction of  $x$  is **true**, we only know that the value of  $x$  is zero or positive, so that both branches are possible. We thus convert the conditional expression above to:

$$\forall\{x \rightarrow M_1, \mathbf{true} \rightarrow M_2\},$$

where  $M_i$  is an abstraction of  $e_i$ . The expression evaluates to  $\forall_1(M_2)$  if  $x$  is **false**, and  $\forall_2(M_1, M_2)$  otherwise. In this way, a deterministic computation step of a source program may be over-approximated by non-deterministic branches, but it is maintained that one of the branches corresponds to the actual computation.

In contrast, the rule PA-RAND *under*-approximates the computation of a source program. That is because, due to an abstraction, we may not know whether some branch exists. For example, consider the expression **let**  $x : \mathbf{int}[\lambda x. 0 \leq x < y] = *_{\mathbf{int}}$  **in**  $e$ .

$$\begin{aligned} D \text{ (programs)} &::= \{f_i \tilde{x}_i = M_i\}_{i \in \{1 \dots n\}} \\ M \text{ (expressions)} &::= c(M_1, \dots, M_k) \mid y \tilde{V} \\ &\quad \mid \mathbf{let } x = (b_1, \dots, b_k) \mathbf{ in } M \\ &\quad \mid \forall\{\psi_1 \rightarrow M_1, \dots, \psi_k \rightarrow M_k\} \\ &\quad \mid \exists\{\psi_1 \rightarrow M_1, \dots, \psi_k \rightarrow M_k\} \\ b \text{ (Booleans)} &::= \mathbf{true} \mid \mathbf{false} \\ V \text{ (values)} &::= (b_1, \dots, b_k) \mid y \tilde{V} \\ c \text{ (tree constructors)} &::= A \mid \mathbf{Call} \mid \mathbf{End} \mid \forall_i \mid \exists_i \\ \psi \text{ (Boolean exp.)} &::= b \mid \#_i(x) \mid \psi_1 \vee \psi_2 \mid \neg\psi \\ E \text{ (eval. context)} &::= [] \\ &\quad \mid c(M_1, \dots, M_{i-1}, E, M_{i+1}, \dots, M_n) \\ E[\mathbf{let } x = (b_1, \dots, b_k) \mathbf{ in } M] &\rightarrow_D E[(b_1, \dots, b_k)/x]M \\ \frac{f \ x_1 \dots x_k = M \in D}{E[f \ V_1 \dots V_k] \rightarrow_D E[[V_1/x_1, \dots, V_k/x_k]M]} \\ \frac{\{\psi_i \mid i \in \{1, \dots, k\}, \llbracket \psi_i \rrbracket = \mathbf{true}\} = \{\psi_{i_1}, \dots, \psi_{i_\ell}\}}{E[\forall\{\psi_1 \rightarrow M_1, \dots, \psi_k \rightarrow M_k\}] \rightarrow_D E[\forall_\ell(M_{i_1}, \dots, M_{i_\ell})]} \\ \frac{\{\psi_i \mid i \in \{1, \dots, k\}, \llbracket \psi_i \rrbracket = \mathbf{true}\} = \{\psi_{i_1}, \dots, \psi_{i_\ell}\}}{E[\exists\{\psi_1 \rightarrow M_1, \dots, \psi_k \rightarrow M_k\}] \rightarrow_D E[\exists_\ell(M_{i_1}, \dots, M_{i_\ell})]} \end{aligned}$$

Figure 6: The syntax and operational semantics of the language of abstract programs.

The abstraction type  $\mathbf{int}[\lambda x. 0 \leq x < y]$  specifies that the random number  $x$  should be abstracted to the Boolean value representing whether  $0 \leq x < y$  holds. Thus, we basically replace the generation of a random integer with that of a random Boolean. However, depending on the actual value of  $y$ , we may not know whether  $0 \leq x < y$  can be **true**. Thus, for example, if the abstraction type of  $y$  is  $\mathbf{int}[\lambda y. 2 < y]$ , then we abstract the expression to:

$$\exists\{y \rightarrow \mathbf{let } x = \mathbf{true} \mathbf{ in } M, \mathbf{true} \rightarrow \mathbf{let } x = \mathbf{false} \mathbf{ in } M\},$$

where  $M$  is an abstraction of  $e$ . Thus, the branch for  $x = \mathbf{true}$  is created only when (the abstract value of)  $y$  is **true** (which means that  $2 < y$  holds in the original computation). The expression evaluates to  $\exists_2(\mathbf{let } x = \mathbf{true} \mathbf{ in } M, \mathbf{let } x = \mathbf{false} \mathbf{ in } M)$  if  $y = \mathbf{true}$ , and  $\exists_1(\mathbf{let } x = \mathbf{false} \mathbf{ in } M)$  otherwise.

**Example 3.** Recall the program  $P'_3$  in Example 1. Let  $\Gamma$  be

$$\begin{aligned} \mathit{rand} &: (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \star \\ f &: \mathbf{int}[\lambda x. 0 < x] \rightarrow (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \star \\ g &: (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \mathbf{int}[\lambda x. 0 < x] \rightarrow \star \\ \mathit{randpos} &: (\mathbf{int}[\lambda x. 0 < x] \rightarrow \star) \rightarrow \star \\ \mathbf{main} &: \star, \end{aligned}$$

$$\begin{array}{c}
\frac{}{\Gamma \vdash () : \star \rightsquigarrow \mathbf{End}} \quad (\text{PA-UNIT}) \\
\frac{\Gamma \vdash e : \sigma \rightsquigarrow M}{\Gamma \vdash \mathbf{event } A; e : \sigma \rightsquigarrow A(M)} \quad (\text{PA-EVENT}) \\
\frac{\Gamma, x : \mathbf{int}[Q_1, \dots, Q_k] \vdash e : \star \rightsquigarrow M \quad \models b_1 Q_1(a) \wedge \dots \wedge b_k Q_k(a) \Rightarrow \theta_\Gamma \psi_{(b_1, \dots, b_k)} \quad (\text{for each } b_1, \dots, b_k \in \{\mathbf{true}, \mathbf{false}\})}{\Gamma \vdash \mathbf{let } x : \mathbf{int}[Q_1, \dots, Q_k] = a \mathbf{ in } e : \star \rightsquigarrow} \\
\forall \left\{ \begin{array}{l} \psi_{(b_1, \dots, b_k)} \rightarrow \\ \mathbf{let } x = (b_1, \dots, b_k) \mathbf{ in } M \end{array} \middle| b_1, \dots, b_k \in \{\mathbf{true}, \mathbf{false}\} \right\} \quad (\text{PA-SEXP}) \\
\frac{\models a \neq 0 \Rightarrow \theta_\Gamma \psi_1 \quad \models a = 0 \Rightarrow \theta_\Gamma \psi_2 \quad \Gamma \vdash e_1 : \star \rightsquigarrow M_1 \quad \Gamma \vdash e_2 : \star \rightsquigarrow M_2}{\Gamma \vdash \mathbf{if } a \mathbf{ then } e_1 \mathbf{ else } e_2 : \star \rightsquigarrow \forall \{\psi_1 \rightarrow M_1, \psi_2 \rightarrow M_2\}} \quad (\text{PA-IF}) \\
\frac{\Gamma, x : \mathbf{int}[Q_1, \dots, Q_k] \vdash e : \star \rightsquigarrow M \quad \models \theta_\Gamma \psi_{(b_1, \dots, b_k)} \Rightarrow \exists x. b_1 Q_1(x) \wedge \dots \wedge b_k Q_k(x) \quad (\text{for each } b_1, \dots, b_k \in \{\mathbf{true}, \mathbf{false}\})}{\Gamma \vdash \mathbf{let } x : \mathbf{int}[Q_1, \dots, Q_k] = *_{\mathbf{int}} \mathbf{ in } e : \star \rightsquigarrow} \\
\exists \left\{ \begin{array}{l} \psi_{(b_1, \dots, b_k)} \rightarrow \\ \mathbf{let } x = (b_1, \dots, b_k) \mathbf{ in } M \end{array} \middle| b_1, \dots, b_k \in \{\mathbf{true}, \mathbf{false}\} \right\} \quad (\text{PA-RAND}) \\
\frac{\Gamma(y) = x_1 : \sigma_1 \rightarrow \dots \rightarrow x_k : \sigma_k \rightarrow \sigma \quad \Gamma \vdash v_i : [v_1/x_1, \dots, v_{i-1}/x_{i-1}] \sigma_i \rightsquigarrow V_i \quad (\text{for each } i \in \{1, \dots, k\})}{\Gamma \vdash y v_1 \dots v_k : [v_1/x_1, \dots, v_k/x_k] \sigma \rightsquigarrow y V_1 \dots V_k} \quad (\text{PA-APP}) \\
\frac{\{f_i \tilde{x} : \tilde{\sigma}_i \rightarrow \star\}_{i \in \{1, \dots, k\}}, \tilde{x} : \tilde{\sigma}_j \vdash e_i : \star \rightsquigarrow M_i \quad (\text{for each } j \in \{1, \dots, k\})}{\Gamma \vdash \{f_i \tilde{x}_i = e_i\}_{i \in \{1, \dots, k\}} : \{f_i : \tilde{x}_i : \tilde{\sigma}_i \rightarrow \star\}_{i \in \{1, \dots, k\}} \rightsquigarrow \{f_i \tilde{x}_i = \mathbf{Call}(M_i)\}_{i \in \{1, \dots, k\}}} \quad (\text{PA-PROG})}
\end{array}$$

Figure 7: Predicate abstraction rules.

then  $P'_3$  is abstracted to the following program  $D_{P'_3}$  (i.e.,  $\vdash P'_3 : \Gamma \rightsquigarrow D_{P'_3}$ ):

$$\begin{array}{l}
\mathbf{rand } k = \mathbf{Call}(\exists \{\mathbf{true} \rightarrow \mathbf{let } r = \mathbf{false} \mathbf{ in } f \ r \ k, \\
\quad \quad \quad \mathbf{true} \rightarrow \mathbf{let } r = \mathbf{true} \mathbf{ in } f \ r \ k\}) \\
f \ b_{0 < r} \ k = \mathbf{Call}(\forall \{\mathbf{true} \rightarrow B(k(b_{0 < r})), \\
\quad \quad \quad \neg b_{0 < r} \rightarrow A(k(b_{0 < r}))\}) \\
\mathbf{randpos } k = \mathbf{Call}(\mathbf{rand } (g \ k)) \\
g \ k \ b_{0 < x} = \mathbf{Call}(\forall \{b_{0 < x} \rightarrow k \ b_{0 < x}, \neg b_{0 < x} \rightarrow \mathbf{randpos } k\}) \\
\mathbf{main} = \mathbf{Call}(\mathbf{randpos } (\lambda b_{0 < x}. \mathbf{End})).
\end{array}$$

Here, we use type  $\mathbf{int}[\lambda r. 0 < r]$  for the type of  $r$  in the body of  $\mathbf{rand}$ . The constructor  $\mathbf{Call}$  has been inserted to the body of each function definition, according to rule PA-PROG. This ensures that every infinite reduction sequence has an infinite sequence of labels. The tree generated by  $D_{P'_3}$  is shown in Figure 8; we have omitted  $\mathbf{Call}$  nodes for the sake of simplicity.  $\square$

### 3.2 Construction of the Tree Automaton $\mathcal{A}_C$

We now construct a tree automaton  $\mathcal{A}_C$ , which expresses a sufficient condition on the tree generated by  $D_P$  for  $P$  to have a fair infinite execution trace. As already explained, the condition is, informally, that (i) every branch of  $\forall$ -node has a fair infinite path, and (ii) at least one of the branches of  $\exists$ -node has a fair infinite

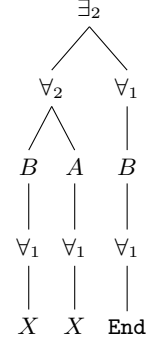


Figure 8: The tree generated by  $D_{P'_3}$ . Here  $X$  is the same as the whole tree.

path. That condition can be easily expressed by a Streett automaton (for infinite trees). For the sake of simplicity, we assume below that the arities of all the  $\forall$  and  $\exists$  nodes are 2; note that  $\forall_i(T_1, \dots, T_i)$  can be replaced by  $\forall_2(T_1, \forall_2(T_2, \forall_2(\dots \forall_2(T_{i-1}, T_i) \dots)))$ .

We first review the definition of Streett automata. We write  $\mathcal{P}(X)$  for the powerset of  $X$ .

**Definition 3.1** ( $\Sigma$ -labeled trees). A *tree* is a set  $f$  of sequences of positive integers, such that whenever  $\pi i \in f$ ,  $\{\pi\} \cup \{\pi j \mid 1 \leq j < i\} \subseteq f$ . Let  $\Sigma$  be a ranked alphabet, i.e., a map from a finite set of symbols to the set of non-negative integers (called arities). A  $\Sigma$ -labeled tree is a map  $T$  from a tree to  $\text{dom}(\Sigma)$ , such that for every  $\pi \in \text{dom}(T)$ ,  $\{i \mid \pi i \in \text{dom}(T)\} = \{1, \dots, \Sigma(T(\pi))\}$ .

**Definition 3.2** (Streett automata). Let  $\Sigma$  be a ranked alphabet. A non-deterministic Streett tree automaton is a quintuple  $\mathcal{A} = (\Sigma, Q, \delta, q_0, C)$  where

- $\Sigma$  is a ranked alphabet,
- $Q$  is a finite set of states,
- $\delta$ , called a *transition function*, is a map from  $Q \times \text{dom}(\Sigma)$  to  $\mathcal{P}(Q^*)$ . Whenever  $q_1 \dots q_\ell \in \delta(q, a)$ , it must be the case that  $\ell = \Sigma(a)$ .
- $q_0 \in Q$  is an initial state, and
- $C$ , called a Streett acceptance condition, is of the form  $\{(E_1, F_1), \dots, (E_k, F_k)\}$ , where  $E_i, F_i \subseteq Q$ .

A *run-tree*  $R$  of  $\mathcal{A}$  over a  $\Sigma$ -labeled tree  $T$  is a tree obtained from  $T$  by annotating each node of  $T$  with an element of  $Q$ , so that

- The root node is annotated with  $q_0$
- If a node with label  $a$  is annotated with  $q$ , then there must exist  $q_1 \dots q_\ell \in \delta(q, a)$  such that the  $i$ -th child of the node is annotated with  $q_i$  for each  $i \in \{1, \dots, \ell\}$ .

A run-tree  $R$  is *accepting* if, for every infinite path  $\pi$  of  $R$  and every  $i \in \{1, \dots, k\}$ , if an element of  $E_i$  occurs infinitely often in  $\pi$ , then some element of  $F_i$  must also occur infinitely often in  $\pi$ . We write  $\text{Lang}(\mathcal{A})$  for a set of trees accepted by  $\mathcal{A}$ .

We can now define  $\mathcal{A}_C$  as a Streett automaton for a fairness constraint  $C$ .

**Definition 3.3** ( $\mathcal{A}_C$ ). Let  $\mathbf{Ev}$  be the finite set of event names that occur in a given program, and let  $C = \{(A_1, B_1), \dots, (A_n, B_n)\}$  (where  $\{A_1, B_1, \dots, A_n, B_n\} \subseteq \mathbf{Ev}$ ) be a fairness constraint. A Streett tree automaton  $\mathcal{A}_C$  is defined by:

$$\mathcal{A}_C = (\Sigma, \{q_0, q_\perp\} \cup \{q_A \mid A \in \mathbf{Ev}\}, \delta, q_0, C)$$

where

$$\begin{aligned} \Sigma &= \{\text{End} \mapsto 0, \text{Call} \mapsto 1, \forall \mapsto 2, \exists \mapsto 2\} \cup \\ &\quad \{A \mapsto 1 \mid A \in \mathbf{Ev}\} \\ \delta(q, \text{End}) &= \begin{cases} \{\epsilon\} & \text{if } q = q_{\perp} \\ \emptyset & \text{otherwise} \end{cases} \\ \delta(q, \text{Call}) &= \begin{cases} \{q_{\perp}\} & \text{if } q = q_{\perp} \\ \{q_0\} & \text{otherwise} \end{cases} \\ \delta(q, \forall) &= \begin{cases} \{q_{\perp} q_{\perp}\} & \text{if } q = q_{\perp} \\ \{q_0 q_0\} & \text{otherwise} \end{cases} \\ \delta(q, \exists) &= \begin{cases} \{q_{\perp} q_{\perp}\} & \text{if } q = q_{\perp} \\ \{q_0 q_{\perp}, q_{\perp} q_0\} & \text{otherwise} \end{cases} \\ \delta(q, A) &= \begin{cases} \{q_{\perp}\} & \text{if } q = q_{\perp} \\ \{q_A\} & \text{otherwise} \end{cases} \\ \mathcal{C} &= \{(\{q_{A_1}\}, \{q_{B_1}\}), \dots, (\{q_{A_n}\}, \{q_{B_n}\})\}. \end{aligned}$$

The automaton just sets the next state to the event label of the current node, and then checks that the fairness condition holds by using the acceptance condition. Upon visiting a  $\forall$ -node, the automaton checks every branch, whereas upon visiting a  $\exists$ -node, it picks only one of branches, and ignores the other branch (with state  $q_{\perp}$ , from which any tree is accepted).

The following theorem states the soundness of our reduction.

**Theorem 1** (Soundness). *Let  $P$  be a program and  $\mathcal{C}$  be a fairness constraint. If  $\mathbf{Tree}(D_P) \in \text{Lang}(\mathcal{A}_{\mathcal{C}})$ , then  $P$  is NOT fair terminating under  $\mathcal{C}$ .*

**Proof sketch.** Let  $e$  be an expression of the source language and suppose that  $\vdash P : \Gamma \rightsquigarrow D_P$  and  $\Gamma \vdash e : \star \rightsquigarrow M$ . Then, the reductions of  $M$  can be simulated by those of  $e$  in the following sense.

- If  $M \longrightarrow^* \forall_k(M_1, \dots, M_k)$ ,<sup>4</sup> then there exists  $i$  such that  $e \xrightarrow{\epsilon}_{\mathcal{P}}^* e', \Gamma \vdash e' : \star \rightsquigarrow M'_i$ , and  $M_i \longrightarrow^* M'_i$  for some  $e'$  and  $M'_i$ .
- If  $M \longrightarrow^* \exists_k(M_1, \dots, M_k)$ , then for every  $i \in \{1, \dots, k\}$ , there exist  $e'$  and  $M'_i$  such that  $e \xrightarrow{\epsilon}_{\mathcal{P}}^* e', \Gamma \vdash e' : \star \rightsquigarrow M'_i$ , and  $M_i \longrightarrow^* M'_i$ .
- If  $M \longrightarrow^* A(M')$ , then there exist  $e'$  and  $M''$  such that  $e \xrightarrow{A}_{\mathcal{P}}^* e', \Gamma \vdash e' : \star \rightsquigarrow M''$ , and  $M' \longrightarrow^* M''$ .
- If  $M \longrightarrow^* \text{Call}(M')$ , then there exist  $e'$  and  $M''$  such that  $e \xrightarrow{\epsilon}_{\mathcal{P}}^+ e', \Gamma \vdash e' : \star \rightsquigarrow M''$ , and  $M' \longrightarrow^* M''$ .

We omit the proof of these facts since it is almost the same as that of the corresponding theorem in [22]. Now, suppose that  $\mathbf{Tree}(D_P)$  is accepted by  $\mathcal{A}_{\mathcal{C}}$ , i.e., there exists an accepting run-tree  $R$  of  $\mathcal{A}_{\mathcal{C}}$  over  $\mathbf{Tree}(D_P)$ . Let  $e = M = \mathbf{main}$ . We can construct a fair infinite execution path of  $P$  from the triple  $(e, M, R)$  as follows.

- If  $R = \forall_k^q(R_1, \dots, R_k)$ , then  $M \longrightarrow^* \forall_k(M_1, \dots, M_k)$ , where  $R_i$  is a run-tree for the tree generated by  $M_i$ , and the root of each  $R_i$  is annotated with  $q_0$ . By the simulation property above, there must be a corresponding execution sequence  $e \xrightarrow{\epsilon}_{\mathcal{P}}^* e'$  such that  $e'$  “simulates”  $M_i$ . Thus, we continue the construction for  $(e', M_i, R_i)$ .
- If  $R = \exists_k^q(R_1, \dots, R_k)$ , then  $M \longrightarrow^* \exists_k(M_1, \dots, M_k)$  where  $R_i$  is a run-tree for the tree generated by  $M_i$ . By the construction of  $\mathcal{A}_{\mathcal{C}}$ , there exists  $i$  such that the root of  $R_i$  is annotated with  $q_0$ . By the simulation property above, there must be a corresponding execution sequence  $e \xrightarrow{\epsilon}_{\mathcal{P}}^* e'$  such

<sup>4</sup>Here, for the sake of simplicity, we assume  $\mathcal{A}_{\mathcal{C}}$  is extended to handle  $k$ -ary  $\forall$  and  $\exists$  symbols directly, without the encoding using binary symbols.

that  $e'$  simulates  $M_i$ . Thus, we continue the construction for  $(e', M_i, R_i)$ .

- If  $R = A^q(R_1)$ , then  $M \longrightarrow^* A(M_1)$  where  $R_1$  is a run-tree for the tree generated by  $M_1$ , and the root of  $R_1$  is annotated with  $q_A$ . By the simulation property above, there must exist  $e'$  such that  $e \xrightarrow{A}_{\mathcal{P}}^* e'$  and  $e'$  simulates  $M_1$ .
- If  $R = \text{Call}^q(R_1)$ , then  $M \longrightarrow^* \text{Call}(M_1)$  where  $R_1$  is a run-tree for the tree generated by  $M_1$ , and the root of  $R_1$  is annotated with  $q_0$ . By the simulation property above, there must exist  $e'$  such that  $e \xrightarrow{\epsilon}_{\mathcal{P}}^+ e'$  and  $e'$  simulates  $M_1$ .

Since  $R$  is an accepting run-tree, we can continue the process above indefinitely and obtain an infinite execution sequence

$$\mathbf{main} \xrightarrow{A_1}_{\mathcal{P}}^* e_1 \xrightarrow{A_2}_{\mathcal{P}}^* e_2 \xrightarrow{A_3}_{\mathcal{P}}^* e_3 \xrightarrow{A_4}_{\mathcal{P}}^* \dots$$

Since  $R$  satisfies the acceptance condition, the sequence of events  $A_1 A_2 A_3 A_4 \dots$  must be fair.  $\square$

## 4. Step 2: Higher-Order Model Checking

The task of Step 2 is, given an abstract program  $D_P$  and a tree automaton  $\mathcal{A}_{\mathcal{C}}$  produced in Step 1, to check whether the tree generated by  $D_P$  (i.e.,  $\mathbf{Tree}(D_P)$ ) is accepted by  $\mathcal{A}_{\mathcal{C}}$ , and generate a counterexample if the answer is negative. Whether  $\mathbf{Tree}(D_P) \in \text{Lang}(\mathcal{A}_{\mathcal{C}})$  holds can be decided by using an existing APT higher-order model checker [9, 10, 25], but we need to add the functionality to generate a counterexample. Below we briefly review higher-order model checking [28] and explain how to use it to decide  $\mathbf{Tree}(D_P) \in \text{Lang}(\mathcal{A}_{\mathcal{C}})$  in Section 4.1. We then describe the new method for counterexample generation in Section 4.2.

### 4.1 Higher-Order Model Checking

Higher-order model checking is the problem of deciding whether the tree generated by a given higher-order recursion scheme (HORS) is accepted by a given tree automaton. A HORS is essentially a simply-typed, tree-generating higher-order functional program. It consists of the following mutually recursive function definitions:

$$\begin{aligned} \mathbf{main} &= t_0 \\ f_1 x_{1,1} \dots x_{1,k_1} &= t_1 \\ \dots & \\ f_n x_{n,1} \dots x_{n,k_n} &= t_n \end{aligned}$$

where  $\mathbf{main}$  is the main function (or the start symbol, in the terminology of formal grammars), and  $t_i$  is an applicative term constructed from variables, functions, and tree constructors. Its syntax is given by:

$$t ::= x \mid f \mid A \mid t_1 t_2,$$

where  $A$  ranges over a finite set of tree constructors.<sup>5</sup> It is decidable whether the tree generated by a HORS is accepted by a tree automaton [28], and a few model checkers are available that support alternating parity tree automata (APT) [9, 10, 25]. Thus, we just need to convert  $D_P$  and  $\mathcal{A}_{\mathcal{C}}$  to a HORS and an APT.

An abstract program can be easily converted to HORS, by applying Church encoding to Boolean values. For example, the

<sup>5</sup>The usual convention of HORS is to use upper letters for function symbols (called non-terminals) and lower letters for tree constructors. We use the opposite (lower letters for function symbols and variables, and upper letters for tree constructors), following the usual convention for functional programs.



program:

```

rand k = Call( $\exists\{\text{true} \rightarrow m \text{ true } k, \text{true} \rightarrow m \text{ false } k\}$ )
m r k =  $\forall\{\text{true} \rightarrow B(k r), \neg r \rightarrow A(k r)\}$ 
g x = Call( $\forall\{x \rightarrow \text{End}, \neg x \rightarrow \text{rand } g\}$ )
main = rand g

```

can be converted to:

```

main = rand g
rand k = Call( $\exists_2(m \text{ true } k)(m \text{ false } k)$ )
m r k = if r ( $\forall_1(B(k r))$ )( $\forall_2(B(k r))(A(k r))$ )
g x = Call(if x ( $\forall_1 \text{End}$ )( $\forall_1(\text{rand } g)$ ))
if b x y = b x y
true x y = x
false x y = y.

```

As for tree automata, Streett tree automata and APT are actually equi-expressive, and we can use the standard technique [12] to convert  $\mathcal{A}_C$  to an equivalent APT.

## 4.2 Counterexample Generation

As already explained, when  $\mathbf{Tree}(D_P)$  is not accepted by  $\mathcal{A}_C$ , we need to generate a *counterexample* and pass it to Step 3. By the discussion above, we may assume that  $D_P$  and  $\mathcal{A}_C$  are a HORS and an APT respectively.

A counterexample is a part of the tree  $\mathbf{Tree}(D_P)$  that violates the property described by  $\mathcal{A}_C$ . More formally, let  $\mathcal{A}^\perp$  be the automaton obtained from  $\mathcal{A}$  by adding a new nullary symbol  $\perp$ , which is blindly accepted from any state. A counterexample against  $\mathbf{Tree}(D) \in \text{Lang}(\mathcal{A})$  is a minimal<sup>6</sup> tree  $T$  obtained by replacing some subtrees of  $\mathbf{Tree}(D)$  with  $\perp$ , such that  $T \notin \text{Lang}(\mathcal{A}^\perp)$ . In the case of our problem where  $D = D_P$  and  $\mathcal{A} = \mathcal{A}_C$ , a counterexample is a tree  $T$  obtained from  $\mathbf{Tree}(D_P)$  by replacing all but one branch of each  $\forall$ -node with  $\perp$ , such that every path of  $T$  is either finite or unfair. For example, for the tree (shown in Figure 8) generated by  $D_{P_3}$  in Example 3, a counterexample is the tree obtained by replacing the first branch of every  $\forall_2$ -node with  $\perp$ .

Since a counterexample defined above may be infinite in general, we use a HORS (or, a tree-generating functional program) as a finite representation of a counterexample. For example, the counterexample mentioned above is expressed by the following HORS consisting of a single function **main**:

```

main =  $\exists_2(\forall_2 \perp (A(\forall_1 \text{main}))) (\forall_1(B(\forall_1 \text{End})))$ .

```

We have extended HorSatP [9] with the functionality to generate a counterexample (in the form of a HORS); the same extension would also be applicable to other higher-order model checkers for APT [10, 25]. The key ideas in our counterexample generation are as follows.

- If  $\mathbf{Tree}(D) \notin \text{Lang}(\mathcal{A})$ , then  $\mathbf{Tree}(D)$  is accepted by the complement  $\bar{\mathcal{A}}$  of  $\mathcal{A}$ , and a counterexample tree is the part of  $\mathbf{Tree}(D)$  visited by  $\bar{\mathcal{A}}$  when  $\mathbf{Tree}(D)$  is accepted by  $\bar{\mathcal{A}}$ .<sup>7</sup>
- A HORS representation of a run-tree of  $\bar{\mathcal{A}}$  (which describes how  $\bar{\mathcal{A}}$  traverses nodes of  $\mathbf{Tree}(D)$ ) can be constructed by a technique called *effective selection* [3, 13, 32]. By modifying it (replacing the label of each node of the run-tree with the symbol read by the corresponding transition of the automaton), we can obtain (a HORS representation of) a counterexample.

<sup>6</sup> With respect to the least compatible relation such that  $\perp \leq T$  for every tree  $T$ .

<sup>7</sup> To ensure the minimality, we need to assume that  $\bar{\mathcal{A}}$  visits as few nodes as possible. Fortunately, for  $\mathcal{A}_C$ , it is easy to guarantee this:  $\bar{\mathcal{A}}_C$  should visit only one of the subtrees for each  $\forall$ -node.

- HorSatP (as well as other APT higher-order model checkers [10, 25]) is based on Kobayashi and Ong’s type system [18] and constructs a type derivation (more precisely, a winning strategy for the typability game [18]). Actually, the soundness proof of Kobayashi and Ong’s type system [18] provides an effective algorithm for converting the type derivation to a run-tree of  $\mathcal{A}$ , which serves as a realistic algorithm for effective selection.

The detail of the counterexample generation method is deferred to a full version of the paper, as understanding it requires knowledge of APT and Kobayashi and Ong’s type system [18].

## 5. Step 3: Predicate Discovery

The task of Step 3 is, given a counterexample tree produced by Step 2, to find new predicates (or abstraction types, more precisely) to refine the predicate abstraction. Assume that a source program  $P$  has a fair non-terminating execution sequence, but that Step 2 produces a counterexample. There are the following three possible cases:

(I) A spurious error path (i.e., an unfair infinite path or a finite path ending with **End** that does not correspond to any actual execution trace of  $P$ ) has been introduced by an over-approximation. For example, consider the following expression of a source program:

```

if b then fair_nonterminating() else
  unfair_nonterminating().

```

Suppose that  $b$  always evaluates to **true** in the source program, but we cannot infer so during predicate abstraction. Then, the tree generated by the abstract program is:  $\forall_2(T_1, T_2)$ , where  $T_1$  has a fair infinite path, but  $T_2$  has only an unfair infinite path. Since we require that both of the branches of  $\forall_2$  has a fair infinite path, the tree is rejected and  $\forall_2(\perp, T_2)$  is generated as a counterexample. In this case, we need to find a predicate necessary to infer that the condition  $b$  is always **true**.

(II) A fair non-terminating execution sequence has been merged with an unfair or terminating one by an approximation of random integers. For example, consider the expression:

```

let x : int[] = *_int in
if 0 < x then fair_nonterminating() else
  unfair_nonterminating().

```

Here, the random number  $x$  is abstracted according to an empty set of predicates. Then, the tree generated by the abstract program is:  $\exists_1(\forall_2(T_1, T_2))$ , where  $T_1$  has a fair infinite path, but  $T_2$  has only an unfair infinite path. Note that no information is available for deciding whether the condition  $0 < x$  holds, so the conditional branch has been replaced by the  $\forall$ -node with both branches. Thus, the tree is rejected, and  $\exists_1(\forall_2(\perp, T_2))$  is generated as a counterexample. In this case, we need to add the predicate  $\lambda x.0 < x$  to the abstraction type of  $x$ . Then, the computations for the case  $0 < x$  and the case  $x \leq 0$  are split, and the abstract program would generate  $\exists_2(\forall_1(T_1), \forall_1(T_2))$ , which will be accepted by  $\mathcal{A}_C$ . Note that the branching between fair and unfair paths has been moved from the  $\forall$ -node to the  $\exists$ -node here.

(III) A fair non-terminating execution sequence has been removed by an under-approximation. For example, consider the expression:

```

let x : int[ $\lambda x.0 \leq x < y$ ] = *_int in
if 0  $\leq$  x < y then fair_nonterminating() else
  unfair_nonterminating().

```

Here, the random number  $x$  is abstracted according to the predicate  $\lambda x.0 \leq x < y$ . If we do not have enough information to conclude that there exists  $x$  such that  $0 \leq x < y$ , then the expression is

abstracted to:

$$\exists\{\text{true} \rightarrow \text{let } x = \text{false} \text{ in if } x \text{ then } M_1 \text{ else } M_2\},$$

which would generate  $\exists_1(T_2)$  where  $T_2$  contains only an unfair infinite path. In this case, we need to add a predicate on  $y$  needed to decide whether there exists  $x$  such that  $0 \leq x < y$ .

Kuwahara et al. [22] made a similar classification on the first and second cases (where they needed to consider only terminating paths as error paths), but overlooked the third case.

The first and second cases (I) and (II) above can be detected by looking at each path of a counterexample tree. In the first case,  $T_2$  in the example occurs as an *infeasible* (i.e., having no corresponding execution in the source program) unfair or terminating path, and in the second case,  $T_2$  in the example occurs as a *feasible* unfair or terminating path. It is hard to detect the third case (III) from the counterexample tree. We can, however, detect such a possibility during the abstraction; in the rule PA-RAND in Figure 7, we can apply the quantifier elimination to  $\exists x.b_1Q_1(x) \wedge \dots \wedge b_kQ_k(x)$ , and add the resulting predicate to the abstraction type environment  $\Gamma$ . In the example above, the required predicate  $0 < y$  is indeed obtained by the quantifier elimination of  $\exists x.0 \leq x < y$ .

For dealing with cases (I) and (II), we pick each path of the counterexample tree, and check whether a finite prefix of the path of a certain length is feasible or not. If it is infeasible, we can conclude that it belongs to case (I). In that case, we can apply the standard technique [19] to find predicates to exclude out the infeasible path. If the finite prefix is feasible, both cases are possible. We, however, tentatively assume that (II) is the case, and find predicates to avoid the merging of paths; for this purpose, we can apply Kuwahara et al.’s technique for “Type II paths” [22]. During the CEGAR cycle (consisting of Steps 1–3), we gradually increase the length of the prefix. Although each path of a counterexample tree may be infinite, in all the cases (I)–(III), the depth of each problematic  $\exists$ - or  $\forall$ -node is finite, so that we can eventually eliminate it.

**Example 4.** Recall the program  $P'_3$  in Example 1 and the counterexample in Section 4.2. If we pick the path  $(\exists_2\forall_2A\forall_1)^\omega$  then the path is feasible but unfair. This path occurs because there is no information for deciding whether the condition  $0 \leq r$  in the body of  $f$  when  $b_{0 < r} = \text{false}$ . In order to split the case  $0 \leq r$ , we pick a finite prefix of the path, e.g.,  $\exists_2\forall_2A\forall_1$ . We find a sufficient condition  $R(r)$  on the value of  $r$  in the body of  $\text{rand}$ , in order for  $P'_3$  to have a reduction sequence not corresponding to  $\exists_2\forall_2A\forall_1$ . The constraint on  $R$  is expressed by:

$$\forall r.(R(r) \Rightarrow 0 \leq r) \wedge \exists r.R(r).$$

Here, the part  $\forall r.(R(r) \Rightarrow 0 \leq r)$  expresses a sufficient condition for the first branch of the body of function  $f$  to be chosen, and the part  $\exists r.R(r)$  expresses the satisfiability of  $R(r)$ . We can obtain  $R = \lambda r.0 \leq r$  as a solution of the constraint, and add it to the type of  $r$  in the body of  $\text{rand}$ .  $\square$

**Remark 1.** If a given program is actually fair terminating, either the whole CEGAR cycle continues indefinitely, finding new (but useless) predicates repeatedly, or Kuwahara et al.’s procedure for “Type II paths” get stuck. The latter case happens, for example, for the following program  $P$ , which is obviously fair-terminating under  $\{(\text{Never}, A)\}$ :

```
main () = event A; main().
```

In this case, the abstract program  $D_P$  generates the infinite tree  $A(A(A(\dots)))$ , consisting of a single path, which itself is the counterexample generated by a model checker. The path is feasible (so it is not Type I), but since no two paths have been merged by the abstraction, the procedure for Type II path gets stuck, failing to find predicates to split the path. In such a case, our procedure in

Step 3 runs forever, looking for a longer path for which Type I or Type II procedure is applicable.

## 6. Implementation and Experiments

This section reports an implementation and experimental results.

### 6.1 Implementation

We have implemented a tool for disproving fair termination as an extension of MoCHI [19]. It takes a program of a subset of OCaml, annotated with events and a fairness constraint, and tries to prove that the program is not fair terminating under the fairness constraint. Currently, the tool supports only singleton fairness constraints (i.e., those of the form  $\{(A, B)\}$ ). As a higher-order model checker, we have used HorSatP [9] and modified it to enable counterexample generation. We used Z3 [8] as the back-end SMT solver used for predicate abstraction and predicate discovery.

### 6.2 Experiments

We ran our tool against several programs, all of which are fair non-terminating. The benchmark programs are written in an extension of the language in Section 2, where general let-expressions ( $\text{let } x = e_1 \text{ in } e_2$ ), sequential compositions ( $e_1; e_2$ ), etc. are allowed. The experiments were conducted on a machine with Intel Xeon E5-2680 v3 (2.50GHz, 16GB of memory) with timeout of 300 seconds.

Table 1 shows the result of the experiments. The column “program” shows the name of each program. The column “order” shows the largest order of functions in the program. The column “cycle” shows the number of CEGAR cycles. The columns “step 1”, “step 2”, and “step 3” show the times spent for each step, all measured in seconds. The column “total” shows the total running time.

The programs of name *xx-buggy* are variants of the benchmark programs in [24]; the original programs are fair terminating, and we have modified them to introduce fair non-termination. We briefly explain some of the other programs below. All the benchmark programs are available at [http://www-kb.is.s.u-tokyo.ac.jp/~watanabe/fair\\_nonterm/](http://www-kb.is.s.u-tokyo.ac.jp/~watanabe/fair_nonterm/).

The program *loop* is a simple non-terminating program consisting of a random integer generator, a conditional branch and events  $A$  and  $B$ . The program *update-max-CPS* is the following program:

```
ev_a k = event A; k ()
ev_b k = event B; k ()
cont x () = let y = *int in
             if x < y then (f ev_b y) else (f ev_a x)
f ev x = ev (cont x)
main = let r = *int in f ev_a r.
```

The variable  $x$  in this program represents the largest value generated by the random integer generator  $*\text{int}$  so far. The event  $B$  occurs only when the value of  $x$  is updated. Therefore, the program is fair terminating if and only if the set of integers generated by  $*\text{int}$  can generate has no upper bound. Since we haven’t embedded any fairness assumption on  $*\text{int}$  into the program, the program is not fair terminating.

The program *call-twice* is:

```
call_twice g = g (); g ()
f () = let x = *int in
        if x < 0 then (event B; ()) else
        (event A; call_twice f)
main = f ().
```

program	order	cycle	time [sec]			
			step 1	step 2	step 3	total
intro	1	3	1.39	1.64	0.52	3.71
loop	2	2	0.51	0.09	0.10	0.78
loop-CPS	3	2	0.84	0.10	0.23	1.25
nested-if	2	2	0.58	0.17	0.38	1.22
op-loop	2	2	0.97	0.04	0.20	1.30
update-max	1	2	0.51	0.09	0.21	0.89
update-max-CPS	3	2	0.86	0.11	0.37	1.43
call-twice	2	2	0.61	0.08	0.20	0.97
odd-nonterm	1	$\geq 4$	-	-	-	timeout
odd-nonterm-annot	1	1	0.92	4.48	-	5.47
compose	2	2	0.41	0.07	0.17	0.72
murase-repeat-buggy	2	2	0.66	0.06	0.18	0.98
murase-closure-buggy	2	2	0.46	0.04	0.21	0.80
koskinen-1-buggy	2	3	1.86	0.25	0.66	2.96
koskinen-2-buggy	1	5	4.58	3.08	1.45	9.50
koskinen-3-1-buggy	1	4	2.03	1.41	1.23	4.94
koskinen-3-2-buggy	1	$\geq 2$	-	-	-	timeout
koskinen-3-2-buggy-annot	1	1	0.57	0.24	-	0.87
koskinen-3-3-buggy	1	4	2.77	0.61	1.84	5.63

Table 1: Experimental results.

The function  $f$  first generates a random number  $x$ . If  $x > 0$ , the function raises an event  $B$  and terminates; otherwise, it raises an event  $A$  and calls itself twice. If  $*_{\text{int}}$  generates positive and negative integers alternately, the execution is fair but non-terminating.

Our tool could disprove fair termination of the benchmark programs, except `odd-nonterm` and `koskinen-3-2-buggy`. The time-outs for `odd-nonterm` and `koskinen-3-2-buggy` are due to the failure to discover appropriate predicates (to be used for predicate abstraction). To confirm this point, we have prepared annotated versions of `odd-nonterm` and `koskinen-3-2-buggy` (`odd-nonterm-annot` and `koskinen-3-2-buggy-annot`), where predicates are given as hints. Our tool could successfully verify them.

## 7. Related Work

The (automated or non-automated) methods for proving temporal properties of higher-order programs have been studied recently, but to our knowledge, ours is the first method that can be used for disproving general<sup>8</sup> linear-time temporal properties of higher-order programs. Skalka et al. [31], Koskinen and Terauchi [20], and Hofmann and Chen [14] proposed type-based methods for proving temporal properties, and Murase et al. [24] have proposed a fully automated method for proving fair termination. None of those methods can be used for *disproving* temporal properties. Lester et al. [23] have also applied APT higher-order model checking to automated verification of temporal properties. Their technique can be used also for disproving properties, but it is applicable only to finite-data functional programs (where only finite data domains are allowed).

As already mentioned, our method is an extension of the method for disproving termination of higher-order functional programs proposed by Kuwahara et al. [22]. The extension is non-trivial, however.

For imperative programs, several methods [2, 5, 6] have been proposed for automatically disproving fair termination. Atig et al. [2] proposed a method for disproving fair termination, whose target is multi-threaded programs written in a first-order imperative language. Cook et al. [5, 6] proposed methods for proving tempo-

ral properties expressed in fair CTL or CTL\*. Since the negation of fair termination can be represented as a fair CTL formula or a CTL\* formula, their methods can disprove fair termination. Those methods are, however, limited to first-order programs and it is not clear how they can be extended to deal with higher-order programs. In fact, Atig et al.’s technique focuses on detecting fair infinite execution sequences that are “ultimately periodic”, but fair infinite execution sequences of higher-order programs tend not to be so. Cook et al.’s technique [5] requires that the control flow graph of a program is finite. One may think of using defunctionalization [7] to convert higher-order programs to first-order programs and then applying a technique for first-order programs. However, since the defunctionalization typically encodes function closures using recursive data structures, the verification of the resulting program would actually become harder.

To our knowledge, our work is the first serious application of APT higher-order model checking to verification of infinite-data higher-order programs [18, 28]; the previous applications of higher-order model checking (to infinite-data programs) [17, 19, 21, 22, 29, 33] used only a subclass of higher-order model checking called trivial automata model checking. There were a few implementations of APT higher-order model checkers [9, 10, 25], but the implementation of counterexample generation is new.

## 8. Conclusion

We have proposed an automated method for disproving fair termination of higher-order functional programs. Our method is a non-trivial extension of Kuwahara et al.’s method for disproving plain termination [22]. Improving the efficiency of our implementation is left for future work; to that end, a more efficient APT higher-order model checker is required.

There are a number of limitations in our current verification method/implementation, to be addressed in future work. We list some of future work below.

- Tighter integration with fair termination verification [24]: As discussed in Remark 1, our procedure does not terminate when a given program is fair terminating; thus, one has to run our procedure for disproving fair termination and Murase et al.’s procedure for proving termination [24] must be executed in

<sup>8</sup>We focused on fair termination, but as mentioned in Section 1, arbitrary  $\omega$ -regular properties can be reduced to fair termination.

parallel. A tighter integration of the two procedures would be required.

- General temporal property verification: Implementing a tool for general temporal property verification is left for future work. As mentioned in Section 1, in theory, Vardi's technique can be used for reducing verification of arbitrary  $\omega$ -regular properties to fair non-termination, but it is not clear whether the naive reduction yields a practical verification tool.
- Scalability: The current tool works for only small programs; to make the tool scalable, we need to improve the underlying higher-order model checker, predicate discovery engine, etc.
- Relative completeness: It is desirable that the method satisfies relative completeness in some sense, as in other verification methods for higher-order programs [24, 33]. It is left for future work to identify a reasonable condition and prove the relative completeness with respect to it.
- Extension of the target language: Since our method relies on higher-order model checking, dealing with recursively-typed or untyped programs is a fundamental challenge.

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## References

- [1] K. Aehlig. A finite semantics of simply-typed lambda terms for infinite runs of automata. *Logical Methods in Computer Science*, 3(3), 2007.
- [2] M. F. Atig, A. Bouajjani, M. Emmi, and A. Lal. Detecting fair non-termination in multithreaded programs. In *Computer Aided Verification - 24th International Conference, CAV 2012, Proceedings*, volume 7358 of *Lecture Notes in Computer Science*, pages 210–226. Springer, 2012.
- [3] A. Carayol and O. Serre. Collapsible pushdown automata and labeled recursion schemes: Equivalence, safety and effective selection. In *Proceedings of the 27th Annual IEEE Symposium on Logic in Computer Science, LICS 2012*, pages 165–174. IEEE Computer Society, 2012.
- [4] B. Cook, A. Gotsman, A. Podolski, A. Rybalchenko, and M. Y. Vardi. Proving that programs eventually do something good. In *Proceedings of the 34th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, POPL 2007*, pages 265–276. ACM, 2007.
- [5] B. Cook, H. Khlaaf, and N. Piterman. On automation of CTL\* verification for infinite-state systems. In *Computer Aided Verification - 27th International Conference, CAV 2015, Proceedings, Part I*, volume 9206 of *Lecture Notes in Computer Science*, pages 13–29. Springer, 2015.
- [6] B. Cook, H. Khlaaf, and N. Piterman. Fairness for infinite-state systems. In *Tools and Algorithms for the Construction and Analysis of Systems - 21st International Conference, TACAS 2015, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2015, Proceedings*, volume 9035 of *Lecture Notes in Computer Science*, pages 384–398. Springer, 2015.
- [7] O. Danvy and L. R. Nielsen. Defunctionalization at work. In *Proceedings of the 3rd international ACM SIGPLAN conference on Principles and practice of declarative programming, PPDP 2001*, pages 162–174. ACM, 2001.
- [8] L. M. de Moura and N. Bjørner. Z3: an efficient SMT solver. In *Tools and Algorithms for the Construction and Analysis of Systems, 14th International Conference, TACAS 2008, Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2008, Proceedings*, volume 4963 of *Lecture Notes in Computer Science*, pages 337–340. Springer, 2008.
- [9] K. Fujima. HorSatP: A saturation-based higher-order model checker for APT, 2015. Tool available from the author.
- [10] K. Fujima, S. Ito, and N. Kobayashi. Practical alternating parity tree automata model checking of higher-order recursion schemes. In *Programming Languages and Systems - 11th Asian Symposium, APLAS 2013, Proceedings*, volume 8301 of *Lecture Notes in Computer Science*, pages 17–32. Springer, 2013.
- [11] J. Giesl, M. Raffelsieper, P. Schneider-Kamp, S. Swiderski, and R. Thiemann. Automated termination proofs for Haskell by term rewriting. *ACM Trans. Program. Lang. Syst.*, 33(2):7, 2011.
- [12] E. Grädel, W. Thomas, and T. Wilke. *Automata, Logics, and Infinite Games: A Guide to Current Research*, volume 2500 of *Lecture Notes in Computer Science*. Springer, 2002.
- [13] A. Haddad. Model checking and functional program transformations. In *IARCS Annual Conference on Foundations of Software Technology and Theoretical Computer Science, FSTTCS 2013*, volume 24 of *LIPICs*, pages 115–126. Schloss Dagstuhl - Leibniz-Zentrum fuer Informatik, 2013.
- [14] M. Hofmann and W. Chen. Abstract interpretation from büchi automata. In *Joint Meeting of the Twenty-Third EACSL Annual Conference on Computer Science Logic (CSL) and the Twenty-Ninth Annual ACM/IEEE Symposium on Logic in Computer Science (LICS), CSL-LICS 2014*, pages 51:1–51:10. ACM, 2014.
- [15] R. Jhala, R. Majumdar, and A. Rybalchenko. HMC: verifying functional programs using abstract interpreters. In *Computer Aided Verification - 23rd International Conference, CAV 2011, Proceedings*, volume 6806 of *Lecture Notes in Computer Science*, pages 470–485. Springer, 2011.
- [16] G. Kaki and S. Jagannathan. A relational framework for higher-order shape analysis. In *Proceedings of the 19th ACM SIGPLAN international conference on Functional programming, ICFP 2014*, pages 311–324. ACM, 2014.
- [17] N. Kobayashi. Model checking higher-order programs. *J. ACM*, 60(3):20, 2013.
- [18] N. Kobayashi and C. L. Ong. A type system equivalent to the modal mu-calculus model checking of higher-order recursion schemes. In *Proceedings of the 24th Annual IEEE Symposium on Logic in Computer Science, LICS 2009*, pages 179–188. IEEE Computer Society, 2009.
- [19] N. Kobayashi, R. Sato, and H. Unno. Predicate abstraction and CEGAR for higher-order model checking. In *Proceedings of the 32nd ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI 2011*, pages 222–233. ACM, 2011.
- [20] E. Koskinen and T. Terauchi. Local temporal reasoning. In *Joint Meeting of the Twenty-Third EACSL Annual Conference on Computer Science Logic (CSL) and the Twenty-Ninth Annual ACM/IEEE Symposium on Logic in Computer Science (LICS), CSL-LICS 2014*, pages 59:1–59:10. ACM, 2014.
- [21] T. Kuwahara, T. Terauchi, H. Unno, and N. Kobayashi. Automatic termination verification for higher-order functional programs. In *Programming Languages and Systems - 23rd European Symposium on Programming, ESOP 2014, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2014, Proceedings*, volume 8410 of *Lecture Notes in Computer Science*, pages 392–411. Springer, 2014.
- [22] T. Kuwahara, R. Sato, H. Unno, and N. Kobayashi. Predicate abstraction and CEGAR for disproving termination of higher-order functional programs. In *Computer Aided Verification - 27th International Conference, CAV 2015, Proceedings, Part II*, volume 9207 of *Lecture Notes in Computer Science*, pages 287–303. Springer, 2015.
- [23] M. Lester, R. P. Neatherway, C. L. Ong, and S. J. Ramsay. Model checking liveness properties of higher-order functional programs. In *Proceedings of ML Workshop 2011*, 2011.
- [24] A. Murase, T. Terauchi, N. Kobayashi, R. Sato, and H. Unno. Temporal verification of higher-order functional programs. In *Proceedings of the 43rd Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, POPL 2016*, pages 57–68. ACM, 2016.
- [25] R. P. Neatherway and C. L. Ong. TravMC2: higher-order model checking for alternating parity tree automata. In *Proceedings of the 2014 International SPIN Symposium on Model Checking of Software, SPIN 2014*, pages 129–132. ACM, 2014.

- [26] P. C. Nguyen and D. V. Horn. Relatively complete counterexamples for higher-order programs. In *Proceedings of the 36th ACM SIGPLAN Conference on Programming Language Design and Implementation, PLDI 2015*, pages 446–456. ACM, 2015.
- [27] P. C. Nguyen, S. Tobin-Hochstadt, and D. V. Horn. Soft contract verification. In *Proceedings of the 19th ACM SIGPLAN international conference on Functional programming, ICFP 2014*, pages 139–152. ACM, 2014.
- [28] C. L. Ong. On model-checking trees generated by higher-order recursion schemes. In *21th IEEE Symposium on Logic in Computer Science, LICS 2006, Proceedings*, pages 81–90. IEEE Computer Society, 2006.
- [29] C. L. Ong and S. J. Ramsay. Verifying higher-order functional programs with pattern-matching algebraic data types. In *Proceedings of the 38th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, POPL 2011*, pages 587–598. ACM, 2011.
- [30] P. M. Rondon, M. Kawaguchi, and R. Jhala. Liquid types. In *Proceedings of the ACM SIGPLAN 2008 Conference on Programming Language Design and Implementation, PLDI 2008*, pages 159–169. ACM, 2008.
- [31] C. Skalka, S. F. Smith, and D. V. Horn. Types and trace effects of higher order programs. *J. Funct. Program.*, 18(2):179–249, 2008.
- [32] T. Tsukada and C. L. Ong. Compositional higher-order model checking via  $\omega$ -regular games over böhm trees. In *Joint Meeting of the Twenty-Third EACSL Annual Conference on Computer Science Logic (CSL) and the Twenty-Ninth Annual ACM/IEEE Symposium on Logic in Computer Science (LICS), CSL-LICS 2014*, pages 78:1–78:10. ACM, 2014.
- [33] H. Unno, T. Terauchi, and N. Kobayashi. Automating relatively complete verification of higher-order functional programs. In *The 40th Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, POPL 2013*, pages 75–86. ACM, 2013.
- [34] M. Y. Vardi. Verification of concurrent programs: The automata-theoretic framework. *Ann. Pure Appl. Logic*, 51(1-2):79–98, 1991.
- [35] N. Vazou, E. L. Seidel, R. Jhala, D. Vytiniotis, and S. L. P. Jones. Refinement types for Haskell. In *Proceedings of the 19th ACM SIGPLAN international conference on Functional programming, ICFP 2014*, pages 269–282. ACM, 2014.
- [36] H. Zhu, A. V. Nori, and S. Jagannathan. Learning refinement types. In *Proceedings of the 20th ACM SIGPLAN International Conference on Functional Programming, ICFP 2015*, pages 400–411. ACM, 2015.